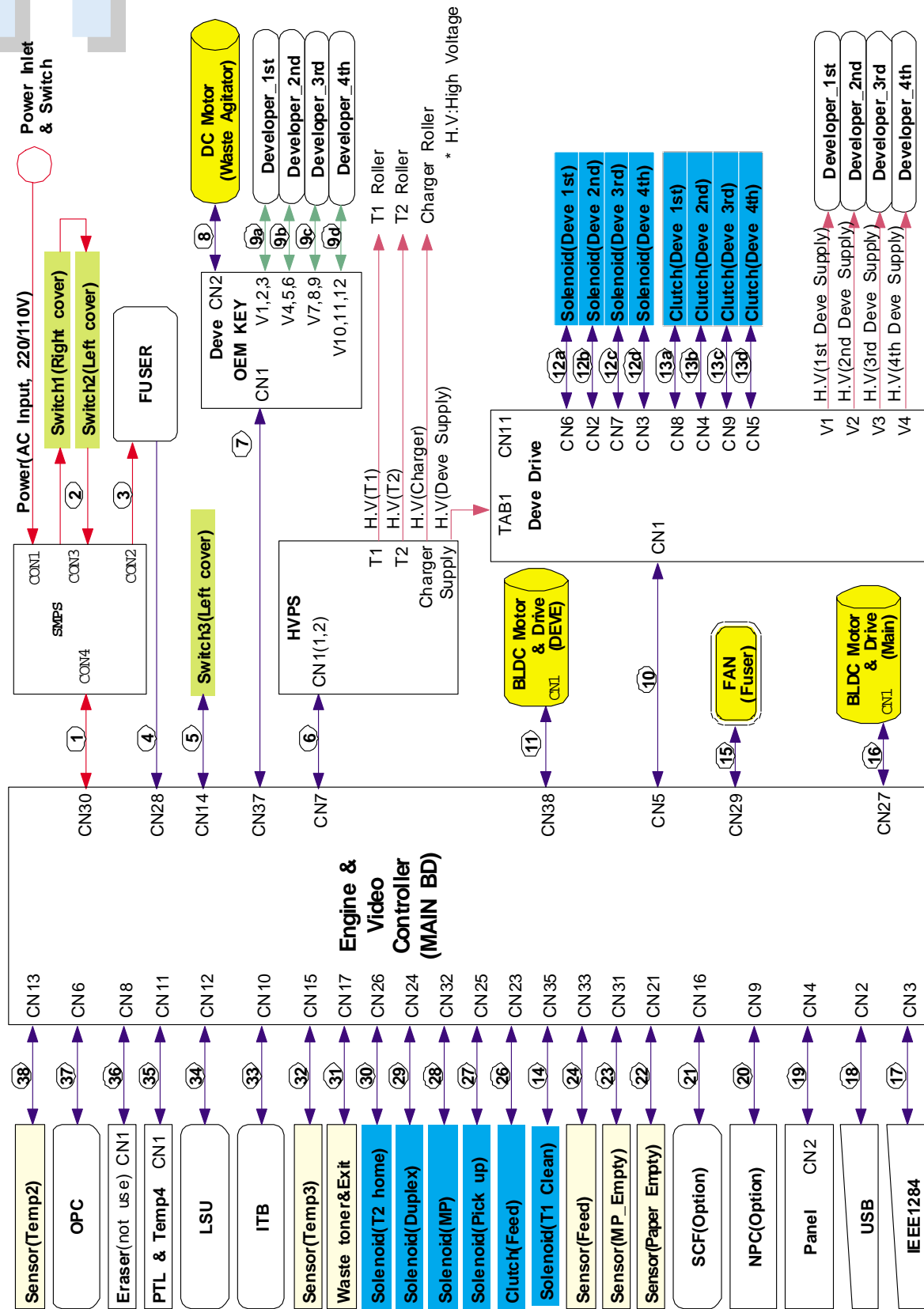


11. Connection Diagram



< Signal Name Table >

* Dir : Signal direction, NC : No Connection
* <- : same signal_name

① Main BD(CN30) ↔ SMP(S(CN4)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+24VF	←	2	+24VF
2	+24VF	←	1	+24VF
3	+24VF	←	4	+24VF
4	+24VF	←	3	+24VF
5	AGND		6	GND
6	AGND		5	GND
7	AGND		8	GND
8	AGND		7	GND
9	+24V	←	10	+24V
10	+24V	←	9	+24V
11	AGND		12	GND
12	AGND		11	GND
13	+3.3V	←	14	+3.3V
14	+3.3V	←	13	+3.3V
15	+3.3V	←	16	+3.3V
16	+3.3V	←	15	+3.3V
17	DGND		18	GND
18	DGND		17	GND
19	DGND		20	GND
20	DGND		19	GND
21	+5V	←	22	+5V
22	+5V	←	21	+5V
23	DGND		24	GND
24	DGND		23	GND
25	nFUSERON	↑	26	<-
26	NC		25	NC

② SMP(S(CN3) ↔ Switch				
Pin	Signal Name	Dir	Pin	Signal Name
1	+24V	→		<-
2	NC			<-
3	+24VF	←		<-

③ SMP(S(CN2) ↔ Fuser Unit				
Pin	Signal Name	Dir	Pin	Signal Name
1	CON2	→		<-
2	NC			<-
3	5096-02C	→		<-

④ Main BD(CN28) ↔ Fuser Unit				
Pin	Signal Name	Dir	Pin	Signal Name
1	AN_FUSER1_OUT	←		<-
2	AN_FUSER1_OUT2	←		<-

⑤ Main BD(CN14) ↔ Switch(Left cover)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+5V	→		<-
2	+5VL	←		<-

Pin	Signal Name	Dir	Pin	Signal Name
2	+24VF	→	1	24V
4	AGND		3	GND
6	PWM_AC_VPP	→	5	AC_Vpp_PWM
8	PWM_T2	→	7	T2_PWM
10	PWM_CHARGE	→	9	CHARGE_PWM
12	ENB_DEVE_AC	→	11	DEV_REM_PWM
14	A_READ_T1	←	13	T1_READ
16	GND	→	15	GND
Main BD(CN7) ↔ HVPS(CN1-2)				
1	+24VF	→	2	24V
3	AGND		4	GND
5	ENB_T2	→	6	T2_EN
7	PWM_AC	→	8	AC_PWM
9	PWM_T1	→	10	T1_PWM
11	PWM_DEVE	→	12	DEVE_PWM
13	A_READ_CHA	←	14	CHARGE_READ
15	+5V	→	16	+5V

⑦ Main Board(CN37) ↔ Deve OEM Key(CN1)				
Pin	Signal Name	Dir	Pin	Signal Name
1	AGND	→	2	AGND
2	+24VF	→	1	+24VF
3	nWST_AGT	→	4	nWST_AGT
4	A_WST_AGT	←	3	A_WST_AGT
5	+5VL	→	6	+5VL
6	Deve_Crum_SCL	→	5	Deve_Crum_SCL
7	Deve_Crum_SDA	↔	8	Deve_Crum_SDA
8	AGND	→	7	AGND

⑧ Deve OEM Key(CN2) ↔ DC Motor (Waste Agitator)				
Pin	Signal Name	Dir	Pin	Signal Name
1	nWST_AGT	→		←
2	NC			←
3	SENSE_AGT	←		←

⑨a Deve OEM Key ↔ Deve Cartridge(1st)				
Pin	Signal Name	Dir	Pin	Signal Name
V1	DEVE_CRUM_SDA	↔		DEVE_CRUM_SDA
V2	DGND	→		DGND
V3	SCL_OUT2	↔		SCL_OUT1

⑨b Deve OEM Key ↔ Deve Cartridge(2nd)				
Pin	Signal Name	Dir	Pin	Signal Name
V4	DEVE_CRUM_SDA	↔		DEVE_CRUM_SDA
V5	DGND	→		DGND
V6	SCL_OUT2	↔		SCL_OUT2

⑨c Deve OEM Key ↔ Deve Cartridge(3rd)				
Pin	Signal Name	Dir	Pin	Signal Name
V7	DEVE_CRUM_SDA	↔		DEVE_CRUM_SDA
V8	DGND	→		DGND
V9	SCL_OUT2	↔		SCL_OUT3

⑨d Deve OEM Key ↔ Deve Cartridge(4th)				
Pin	Signal Name	Dir	Pin	Signal Name
V10	DEVE_CRUM_SDA	↔		DEVE_CRUM_SDA
V11	DGND	→		DGND
V12	SCL_OUT2	↔		SCL_OUT4

⑩ Main Board(CN5) ↔ Deve Drive(CN1)				
Pin	Signal Name	Dir	Pin	Signal Name
1	AGND		2	AGND
2	+24VF	→	1	24V
3	AGND		4	AGND
4	+24VF	→	3	24V
5	nSOL_DEVE_1ST	→	6	AGND
6	nSOL_DEVE_2ND	→	5	24V
7	nSOL_DEVE_3RD	→	8	DGND
8	nSOL_DEVE_4TH	→	7	5V
9	nCLT_DEVE_1ST	→	10	nSOL_DEVE_1ST
10	nCLT_DEVE_2ND	→	9	nSOL_DEVE_2ND
11	nCLT_DEVE_3RD	→	12	nSOL_DEVE_3RD
12	nCLT_DEVE_4TH	→	11	nSOL_DEVE_4TH

⑪ Main Board ↔ BLDC Motor(Deve)				
Pin	Signal Name	Dir	Pin	Signal Name
1	24V	→	1	+24V
2	24V	→	2	+24V
3	AGND		3	PGND
4	AGND		4	PGND
5	DGND		5	SGND
6	5V	→	6	+5V
7	START	→	7	START
8	READY	←	8	READY
9	CLOCK	→	9	CLOCK
			10	CW/CCW

12a Deve Drive(CN6)↔Solenoid(Deve 1st)

Pin	Signal Name	Dir	Pin	Signal Name
1	nSOL_DEV_1ST	→		<-
2	NC			<-
3	AGND			<-

12b Deve Drive(CN2)↔Solenoid(Deve 2nd)

Pin	Signal Name	Dir	Pin	Signal Name
1	nSOL_DEV_2ND	→		<-
2	NC			<-
3	AGND			<-

12c Deve Drive(CN7)↔Solenoid(Deve 3rd)

Pin	Signal Name	Dir	Pin	Signal Name
1	nSOL_DEV_3RD	→		<-
2	NC			<-
3	AGND			<-

12d Deve Drive(CN3)↔Solenoid(Deve 4th)

Pin	Signal Name	Dir	Pin	Signal Name
1	nSOL_DEV_4TH	→		<-
2	NC			<-
3	AGND			<-

13a Deve Drive(CN8)↔Clutch(Deve 1st)

Pin	Signal Name	Dir	Pin	Signal Name
1	nCLT_DEV_1ST	→		<-
2	NC			<-
3	AGND			<-

13b Deve Drive(CN4)↔Clutch(Deve 2nd)

Pin	Signal Name	Dir	Pin	Signal Name
1	nCLT_DEV_2ND	→		<-
2	NC			<-
3	AGND			<-

13c Deve Drive(CN9)↔Clutch(Deve 3rd)

Pin	Signal Name	Dir	Pin	Signal Name
1	nCLT_DEV_3RD	→		<-
2	NC			<-
3	AGND			<-

13d Deve Drive(CN5)↔Clutch(Deve 4th)

Pin	Signal Name	Dir	Pin	Signal Name
1	nCLT_DEV_4TH	→		<-
2	NC			<-
3	AGND			<-

14 Main Board(CN35)↔Solenoid(T1 clean)

Pin	Signal Name	Dir	Pin	Signal Name
1	nSOL_T1_CLN	→		<-
2	NC			<-
3	AGND			<-

15 Main BD(CN29)↔Fan Motor(Fuser)

Pin	Signal Name	Dir	Pin	Signal Name
1	nFAN_FUSER	→		<-
2	NC			<-
3	+24VF	→		<-

16 Main BD(CN27)↔BLDC Motor(Main)

Pin	Signal Name	Dir	Pin	Signal Name
1	+24VF	→	1	+24V
2	+24VF	→	2	+24V
3	AGND		3	PGND
4	AGND		4	PGND
5	DGND		5	SGND
6	+5V	→	6	+5V
7	nBLDC_START1	→	7	START
8	nBLDC_RDY1	←	8	READY
9	BLDC_CLK1	→	9	CLOCK
10	DGND		10	CW/CCW

⑪ Main BD(CN3) ↔ IEEE1284 Port				
Pin	Signal Name	Dir	Pin	Signal Name
1	nSTB	←		←
2	DATA0	↔		←
3	DATA1	↔		←
4	DATA2	↔		←
5	DATA3	↔		←
6	DATA4	↔		←
7	DATA5	↔		←
8	DATA6	↔		←
9	DATA7	↔		←
10	nACK	↑		←
11	BUSY	↑		←
12	PERROR	↑		←
13	SELECT	↑		←
14	nAUTOFD	↓		←
15	NC			←
16	DGND			←
17	AGND			←
18	5V1			←

Pin	Signal Name	Dir	Pin	Signal Name
19	DGND			←
20	DGND			←
21	DGND			←
22	DGND			←
23	DGND			←
24	DGND			←
25	DGND			←
26	DGND			←
27	DGND			←
28	DGND			←
29	DGND			←
30	DGND			←
31	nINIT	↓		←
32	nFAULT	↑		←
33	NC			←
34	NC			←
35	NC			←
36	nSELECTIN	↓		←

⑫ Main BD(CN2) ↔ USB Port				
Pin	Signal Name	Dir	Pin	Signal Name
1	VBUS_2270	←		VBUS
2	DN	↔		D-
3	DP	↔		D+
4	DGND			AGND
5	AGND			FGND
6	AGND			FGND

⑬ Main BD(CN4) ↔ Panel(CN2)				
Pin	Signal Name	Dir	Pin	Signal Name
1	DGND		1	DGND
2	+5V	→	2	VCC
3	PANEL_TX	→	3	OPE_RXD
4	nRST_PANEL	→	4	/OPE_RST
5	PANEL_RX	←	5	OPE_TXD

②① Main BD(CN9) ↔ NPC(J1) (Network Print Card)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+3.3V	→	1	VDD3
2	+3.3V	→	2	VDD3
3	DGND		3	GND
4	nNPC_CS	→	4	nXPCS
5	DATA(31)	↔	5	XPData(31)
6	DATA(30)	↔	6	XPData(30)
7	DATA(29)	↔	7	XPData(29)
8	DGND		8	GND
9	DATA(28)	↔	9	XPData(28)
10	DATA(27)	↔	10	XPData(27)
11	DATA(26)	↔	11	XPData(26)
12	DGND		12	GND
13	DATA(25)	↔	13	XPData(25)
14	DATA(24)	↔	14	XPData(24)
15	nNPC_INT	←	15	nXIRQ_OUT
16	DGND		16	GND
17	A_ADDR(5)	→	17	XPAddr(3)
18	A_ADDR(4)	→	18	XPAddr(2)
19	A_ADDR(3)	→	19	XPAddr(1)
20	+3.3V	→	20	VDD3
21	NC		21	NC
22	DATA(23)	↔	22	XPData(23)
23	DATA(22)	↔	23	XPData(22)
24	NC		24	NC
25	DATA(21)	↔	25	XPData(21)
26	NC		26	NC
27	DATA(20)	↔	27	XPData(20)
28	A_ADDR(15)	→	28	XPAddr(13)
29	DATA(19)	↔	29	XPData(19)
30	A_ADDR(14)	→	30	XPAddr(12)

②② Main BD(CN16) ↔ SCF Unit(CN9) (Secondary Cassette Feeder)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+3.3V	→	1	3.3V
2	STS_SCF	←	2	SCF_STS
3	CMD_SCF	→	3	SCF_CMD
4	CLK_SCF	→	4	SCF_CLK
5	RDY_SCF	←	5	SCF_RDY
6	+24V	→	6	24VS
7	DGND		7	DGND
8	AGND		8	AGND

②③ Main BD(CN21) ↔ Sensor(Paper Empty)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+3.3V	→	1	←
2	nS_EMPTY	←	2	←
3	DGND		3	←
4	NC		4	←

②④ Main BD(CN31) ↔ Sensor(MP Empty)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+3.3V	→	1	←
2	nS_MP_EMPTY	←	2	←
3	DGND		3	←
4	NC		4	←

Pin	Signal Name	Dir	Pin	Signal Name
31	DATA(18)	↔	31	XPData(18)
32	A_ADDR(13)	→	32	XPAddr(11)
33	DATA(17)	↔	33	XPData(17)
34	A_ADDR(12)	→	34	XPAddr(10)
35	DATA(16)	↔	35	XPData(16)
36	A_ADDR(11)	→	36	XPAddr(9)
37	A_ADDR(6)	→	37	XPAddr(4)
38	A_ADDR(10)	→	38	XPAddr(8)
39	nNPC_RST	→	39	nRESET_P
40	A_ADDR(9)	→	40	XPAddr(7)
41	nWAIT	←	41	nXPWAIT
42	A_ADDR(8)	→	42	XPAddr(6)
43	NC		43	NC
44	A_ADDR(7)	→	44	XPAddr(5)
45	NC		45	NC
46	NC		46	NC
47	NC		47	NC
48	DGND		48	GND
49	NC		49	NC
50	nRD	→	50	nXPWE
51	nWR	→	51	nXPWE
52	DGND		52	GND
53	NC		53	NC
54	NC		54	NC
55	NC		55	NC
56	DGND		56	GND
57	nPRT_IRQ	→	57	nXIRQ_IN
58	DGND		58	GND
59	DGND		59	GND
60	DGND		60	GND

②④ Main BD(CN33) ↔ Sensor(Feed)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+3.3V	→	1	<-
2	nS_FEED	←	2	<-
3	DGND		3	<-

②⑥ Main BD(CN23) ↔ Clutch(Feed)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+24VF	→		<-
2	nCLT_FEED	→		<-

②⑦ Main BD(CN25) ↔ Solenoid(Pick up)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+24VF	→		<-
2	nSOL_PICKUP	→		<-

②⑧ Main BD(CN32) ↔ Solenoid(MP)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+24VF	→		<-
2	nSOL_MP	→		<-
3	NC			<-

②⑨ Main BD(CN24) ↔ Solenoid(Duplex)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+24VF	→		<-
2	nSOL_DUP	→		<-

③⑩ Main BD(CN26) ↔ Solenoid(T2 home)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+3.3V	→	1	<-
2	NC		2	<-
3	nSOL_T2	→	3	<-

③① Main BD(CN17) ↔ Sensor(Waste Toner) & Exit Sensor				
Pin	Signal Name	Dir	Pin	Signal Name
1	+3.3V	→	1	<-
2	DGND		2	<-
3	nS_TONER_RX	←	1	<-
4	DGND		2	<-
5	+3.3V	→	1	<-
6	nS_EXIT	←	2	<-
7	DGND		3	<-

③② Main BD(CN15) ↔ Sensor(Temperature)				
Pin	Signal Name	Dir	Pin	Signal Name
1	A_TEMP	←	1	<-
2	GNDA		2	<-

③③ Main BD(CN10) ↔ ITB Unit(Drawer) Connector on Frame)				
Pin	Signal Name	Dir	Pin	Signal Name
1	+5VL	→	J7	<-
2	nS_ITB_HOME	←	J6	<-
3	DGND		J5	<-
4	A_ITB_HOME	←	J4	<-
5	DGND		J3	<-
6	ITB_CRUM_SCL	→	J14	<-
7	ITB_CRUM_SDA	←	J13	<-

③④ Main BD(CN12) ↔ LSU				
* LD Part				
Pin	Signal Name	Dir	Pin	Signal Name
1	nHSYNC	→	1	*HSYNC
2	+5VL	→	2	+5V
3	DGND		3	GND
4	nLDON_LSU	→	4	*LD ON
5	VDO_LSU+	→	5	*VIDEO+
6	VDO_LSU-	→	6	*VIDEO-
7	nSH_LSU	→	7	*S/H
* Motor Driver				
8	CLK_LSU	→	1	CLK
9	nRDY_LSU	←	2	*READY
10	nSTART_LSU	→	3	*START
11	AGND		4	GND
12	+24V	→	5	VCC

③⑦ Main BD(CN6) ↔ OPC Unit				
Pin	Signal Name	Dir	Pin	Signal Name
1	A_OPC_KEY	←		←
2	DGND			←

③⑤ Main BD(CN11) ↔ PTL(CN1)&Temp				
Pin	Signal Name	Dir	Pin	Signal Name
1	+5V	→	1	←
2	EN_ERASER	→	2	←
3	A_TEMP2	←	1	←
4	GNDA		3	←