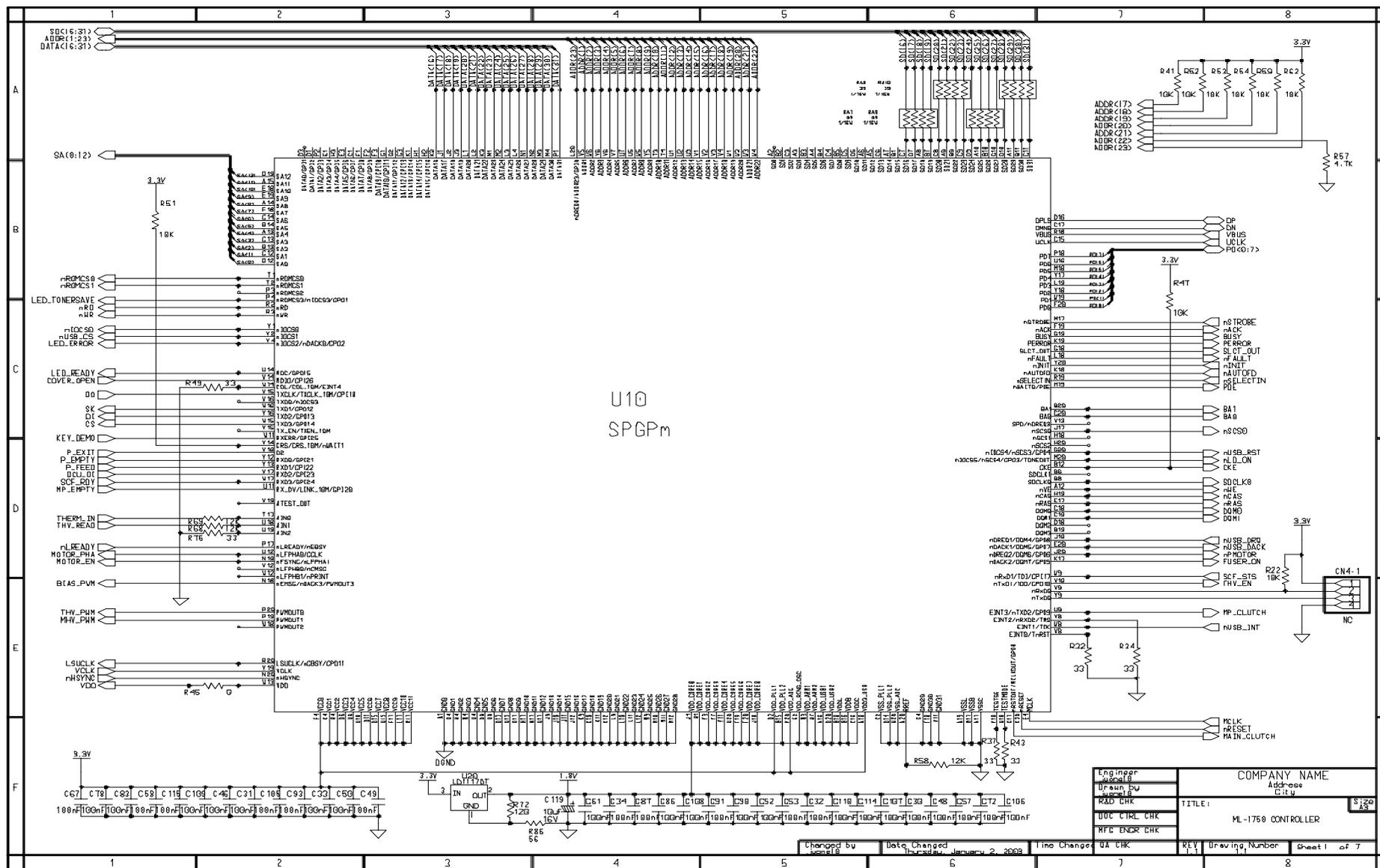
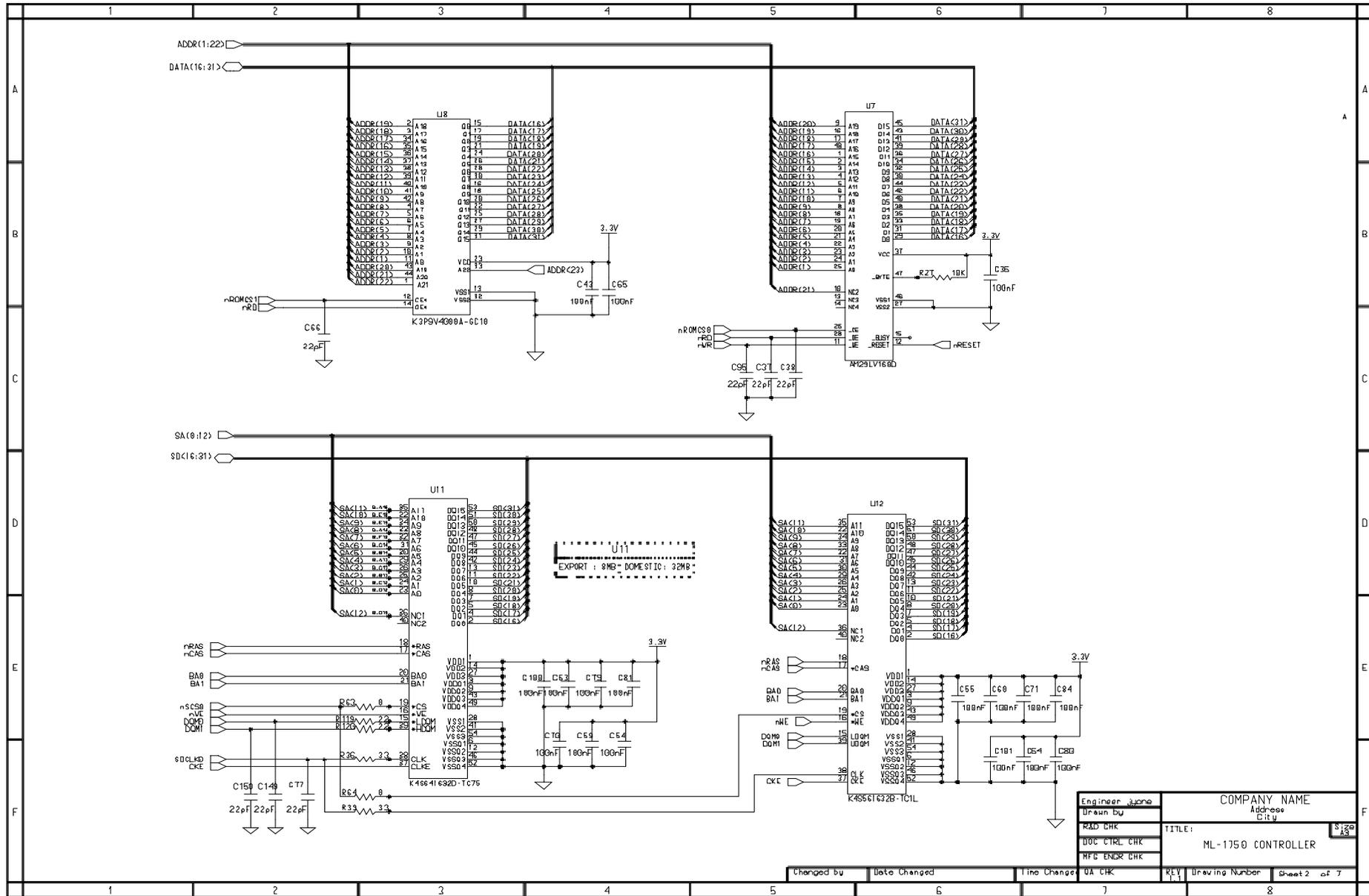


11. Schematic Diagrams

11.1 PCL Main Circuit Diagram (1/7)

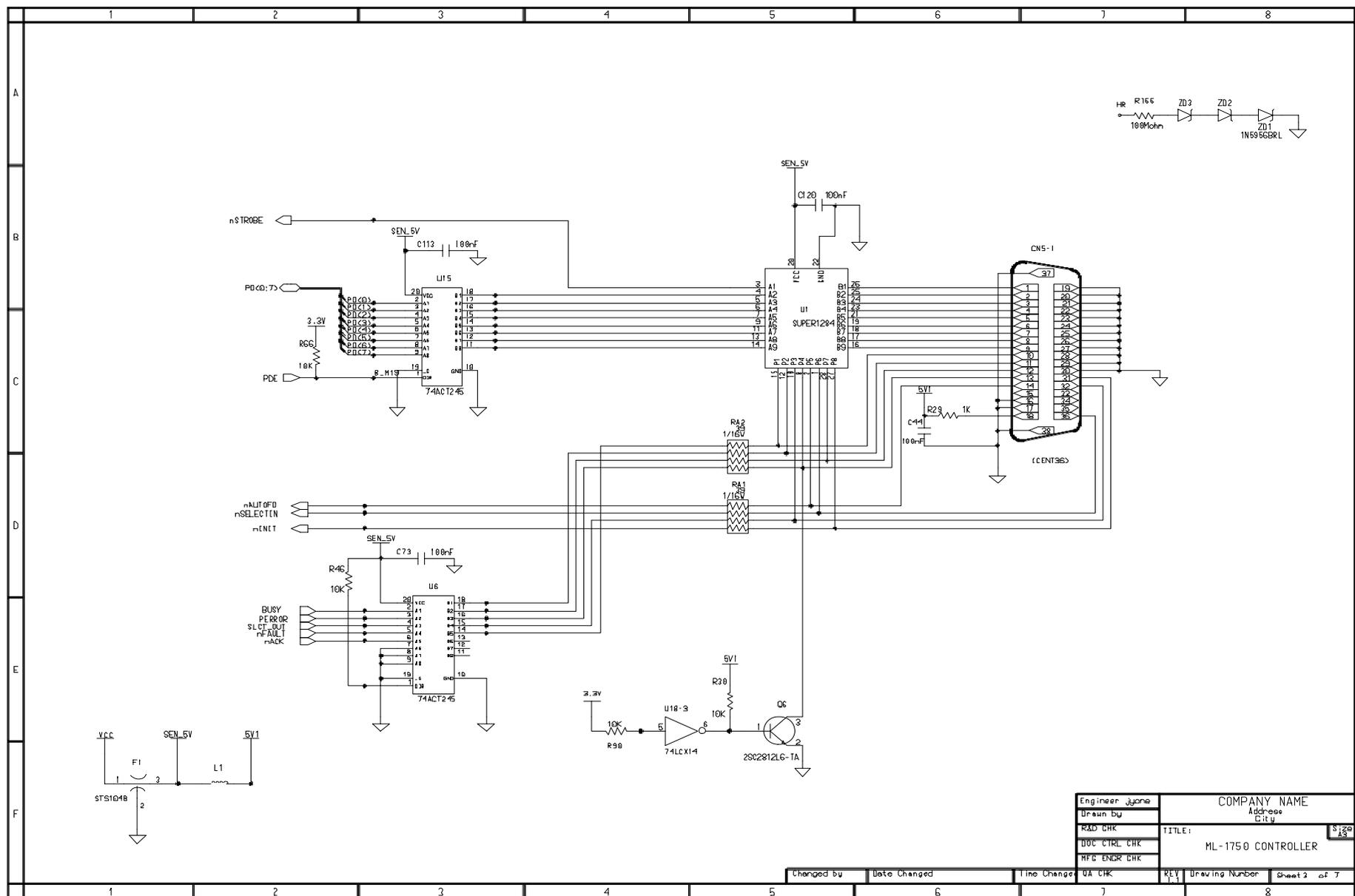


PCL Main Circuit Diagram (2/7)



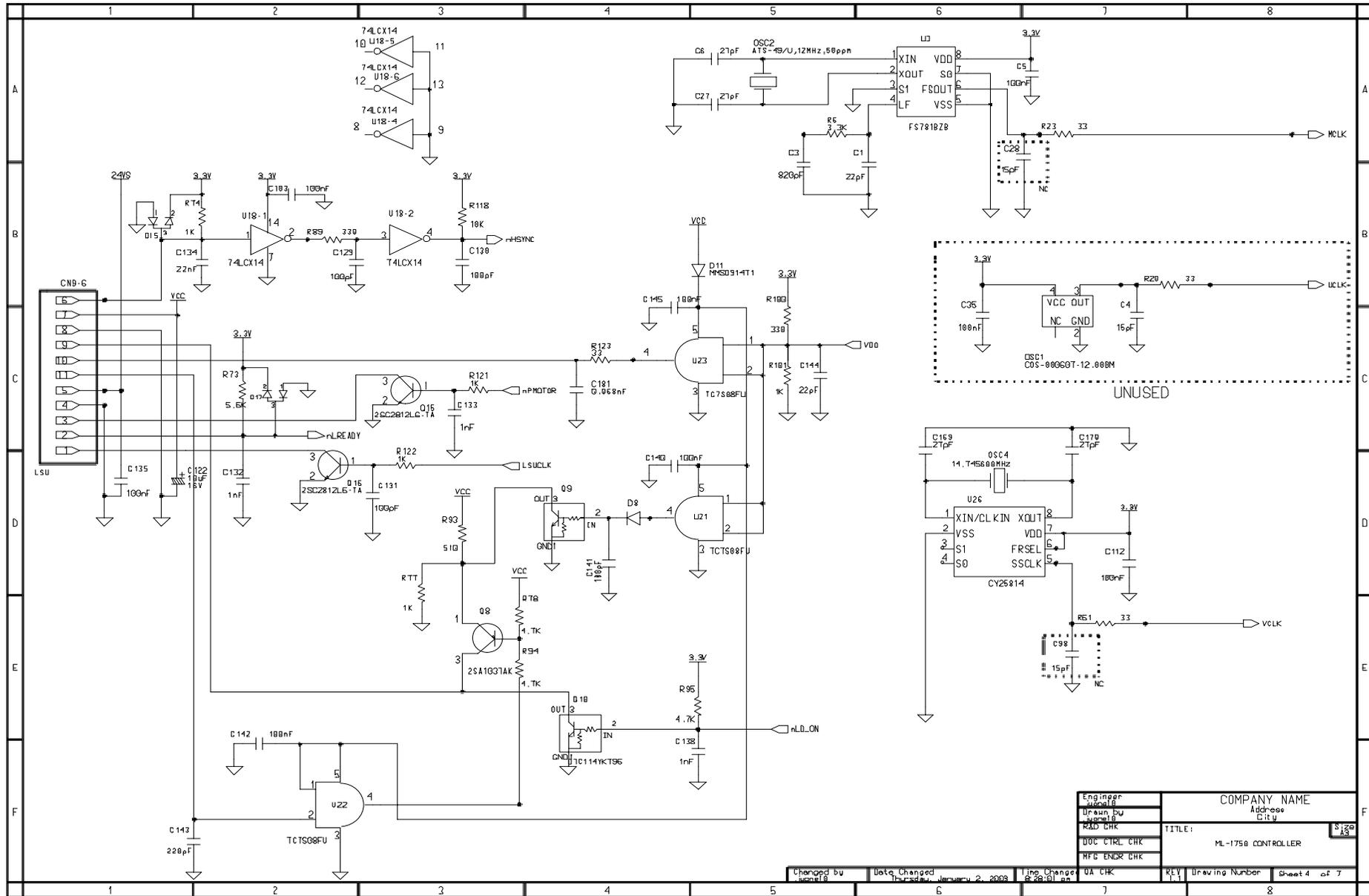
Engineer <i>jujuna</i>	COMPANY NAME	
Drawn by	Address	City
RD CHK	TITLE:	Size
DOC CTRL CHK	ML-1750 CONTROLLER	
RFC ENCR CHK		
Changd by	Date Changd	Time Changd
DA CHK	REV	Drawing Number
		Sheet 2 of 7

PCL Main Circuit Diagram (3/7)



Engineer: <i>kyona</i>	COMPANY NAME
Drawn by:	Address
RAD CHK:	City
DOC CTRL CHK:	TITLE: ML-1750 CONTROLLER
RFQ ENDR CHK:	Size
QA CHK:	Sheet 3 of 7
REV 1	Drawing Number

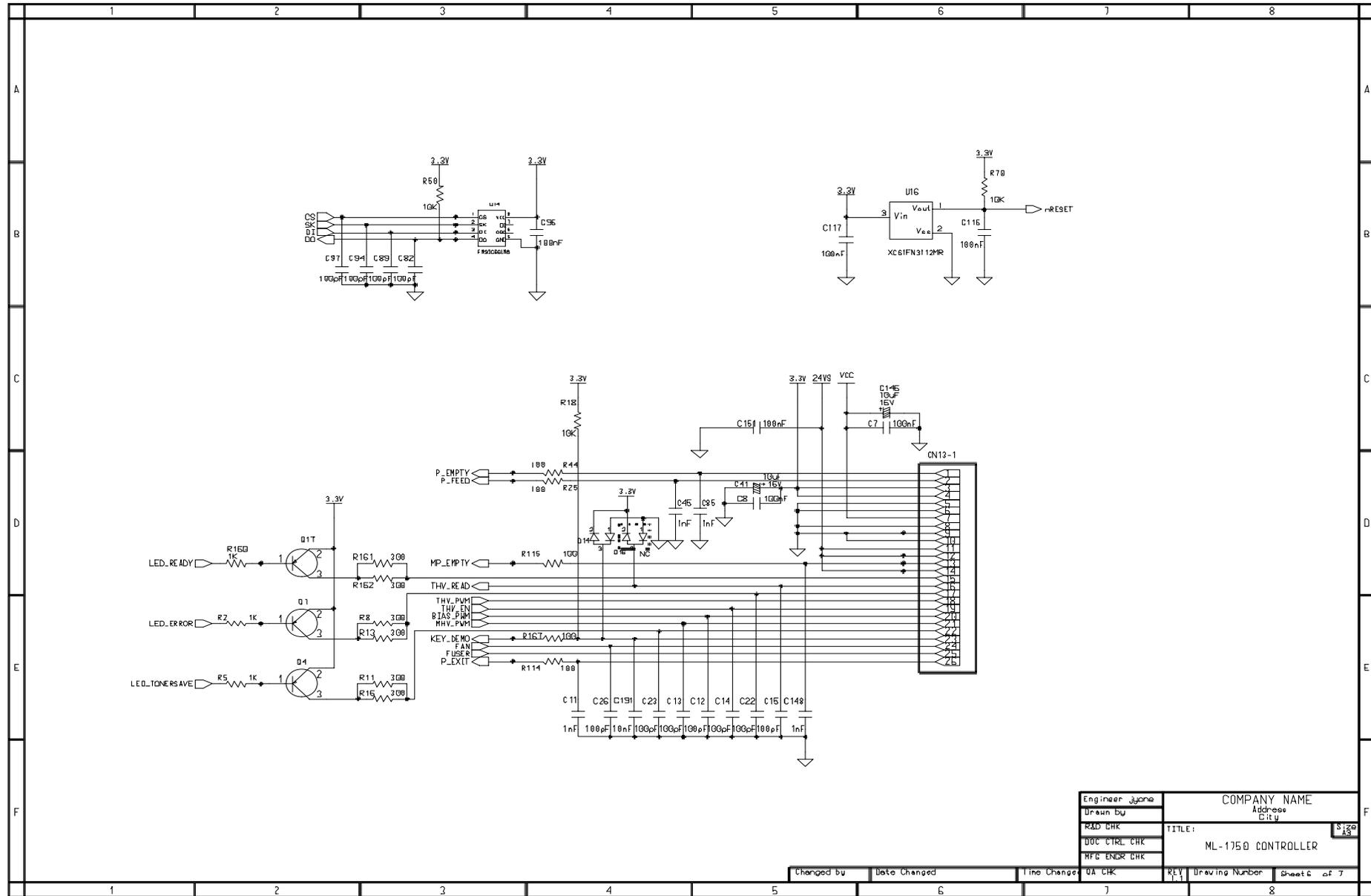
PCL Main Circuit Diagram (4/7)



Engineer	COMPANY NAME
Drawn by	Address
Check	City
DOC CTRL CHK	TITLE: ML-1750 CONTROLLER
MFG ENGR CHK	REV: 1

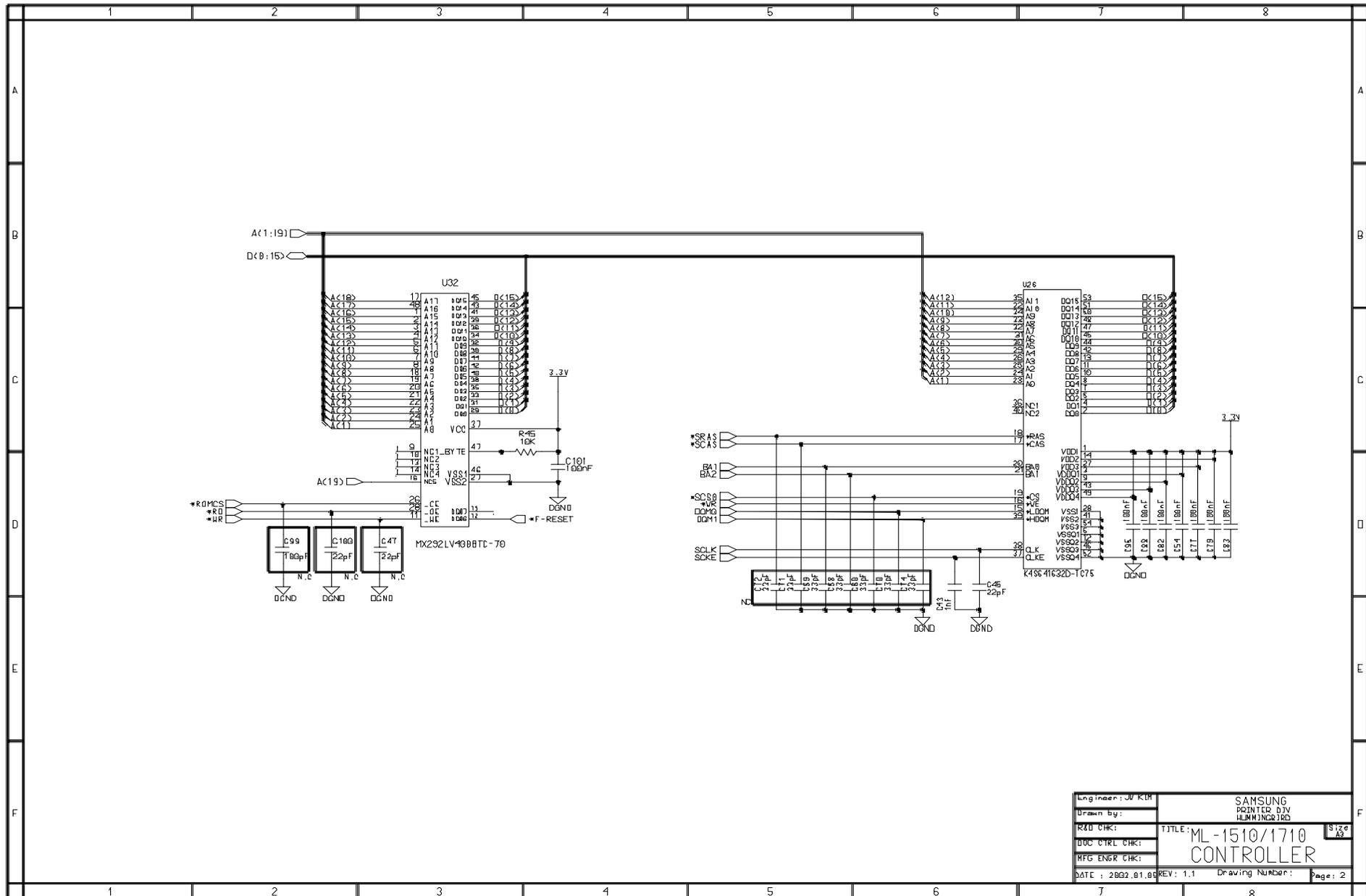
Changed by	Date Changed	Time Change	QA CHK	REV	Drawing Number	Sheet 4 of 7
user18	Thursday, January 2, 2003	8:28:01 am		1		

PCL Main Circuit Diagram (6/7)

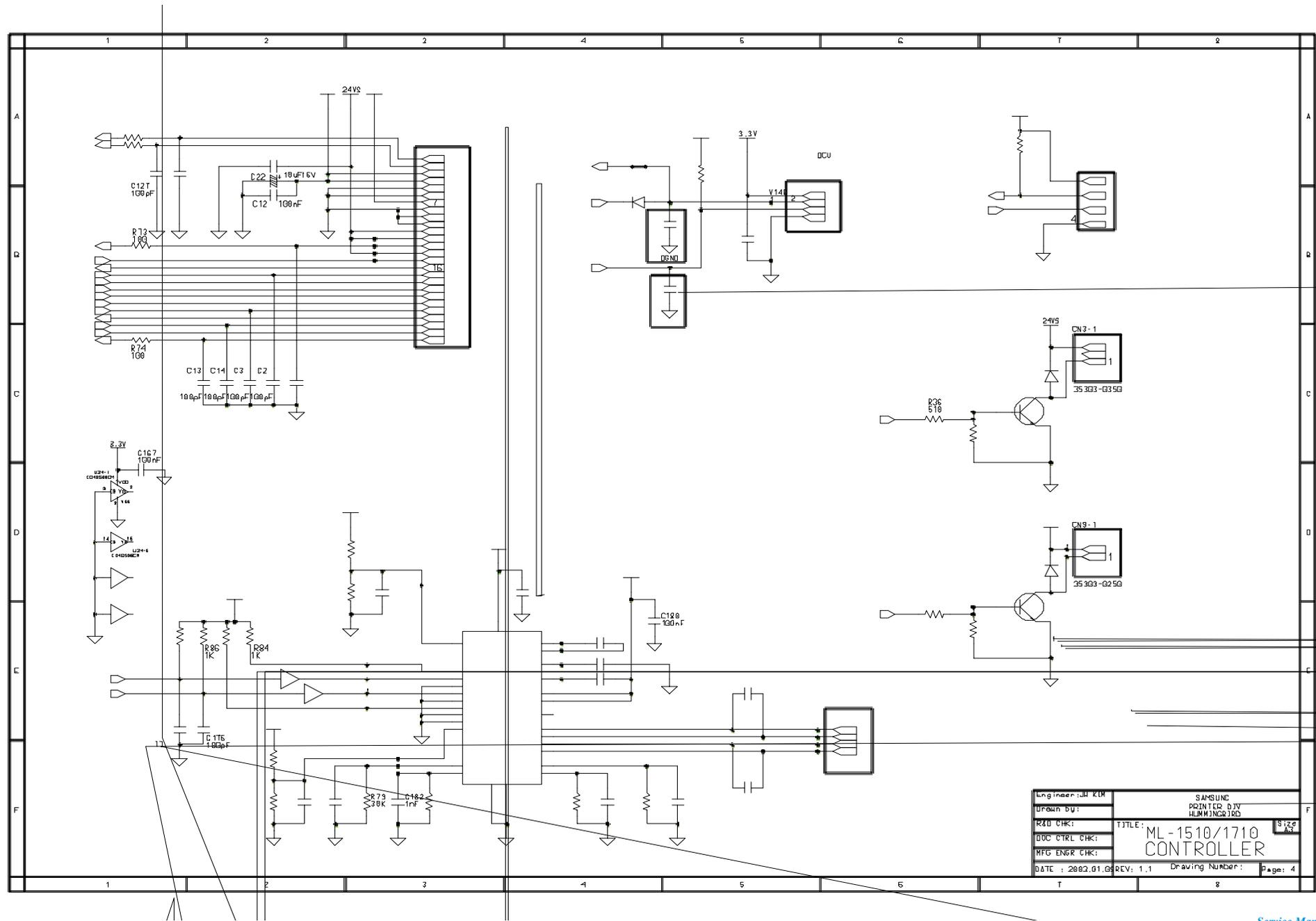


Engineer Jyona	COMPANY NAME
Drawn by	Address
RELD CHK	City
DOC CTRL CHK	TITLE: ML-1750 CONTROLLER
RFIC ENCR CHK	Size 1/16
Changed by	Date Changed
Time Change	DA CHK
REV	Drawing Number
	Sheet of 7

SPL Main Circuit Diagram (2/5)

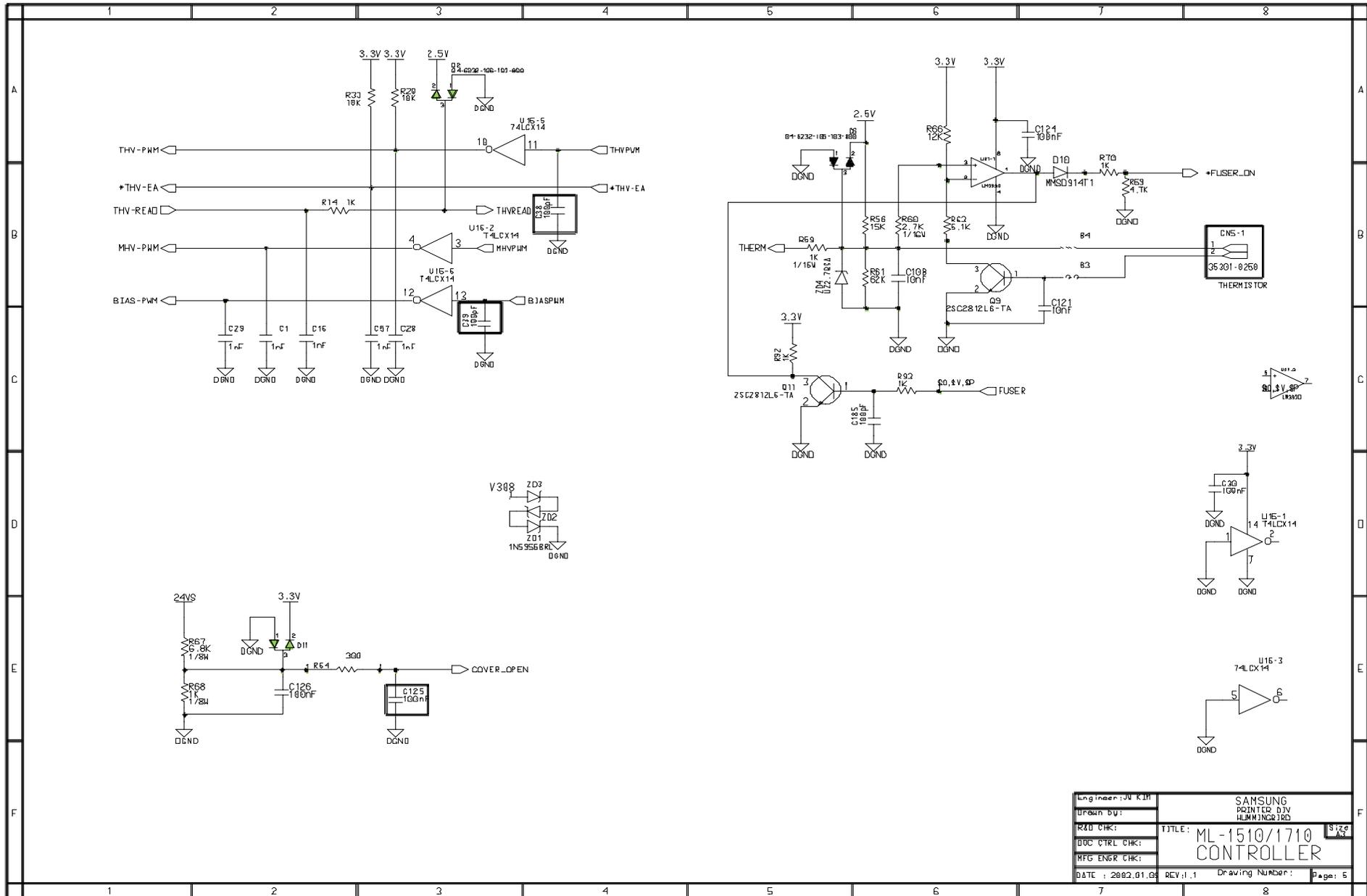


SPL Main Circuit Diagram (4/5)



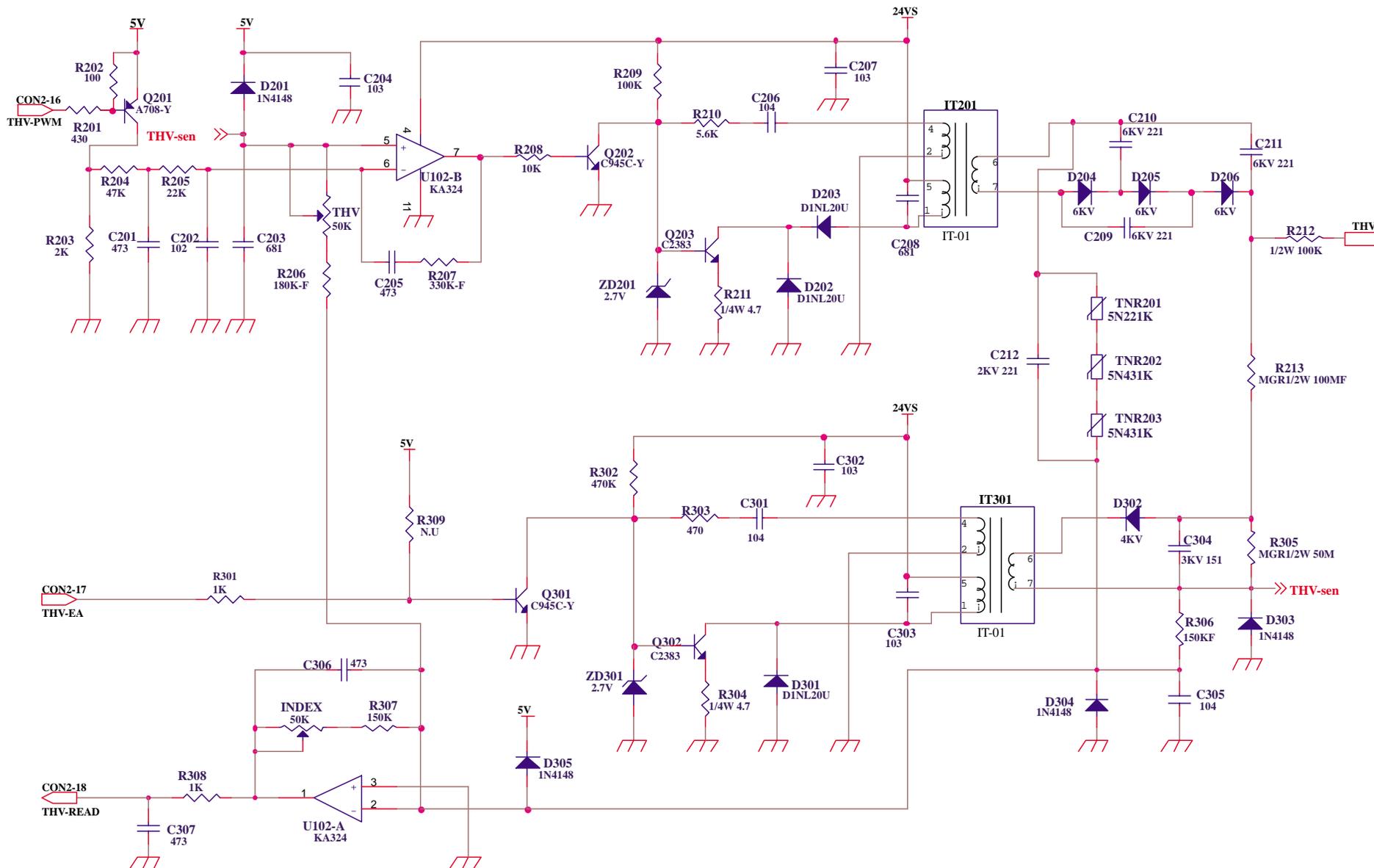
Engineer: J. KIM	SAMSUNG	
Drawn by:	PRINTER, DJV	
R&D CHK:	HUMMINGBIRD	
DOC CTRL CHK:	TITLE: ML-1510/1710	
MFG ENGR CHK:	CONTROLLER	
DATE: 2002.01.09	REV: 1.1	Page: 4

SPL Main Circuit Diagram (5/5)

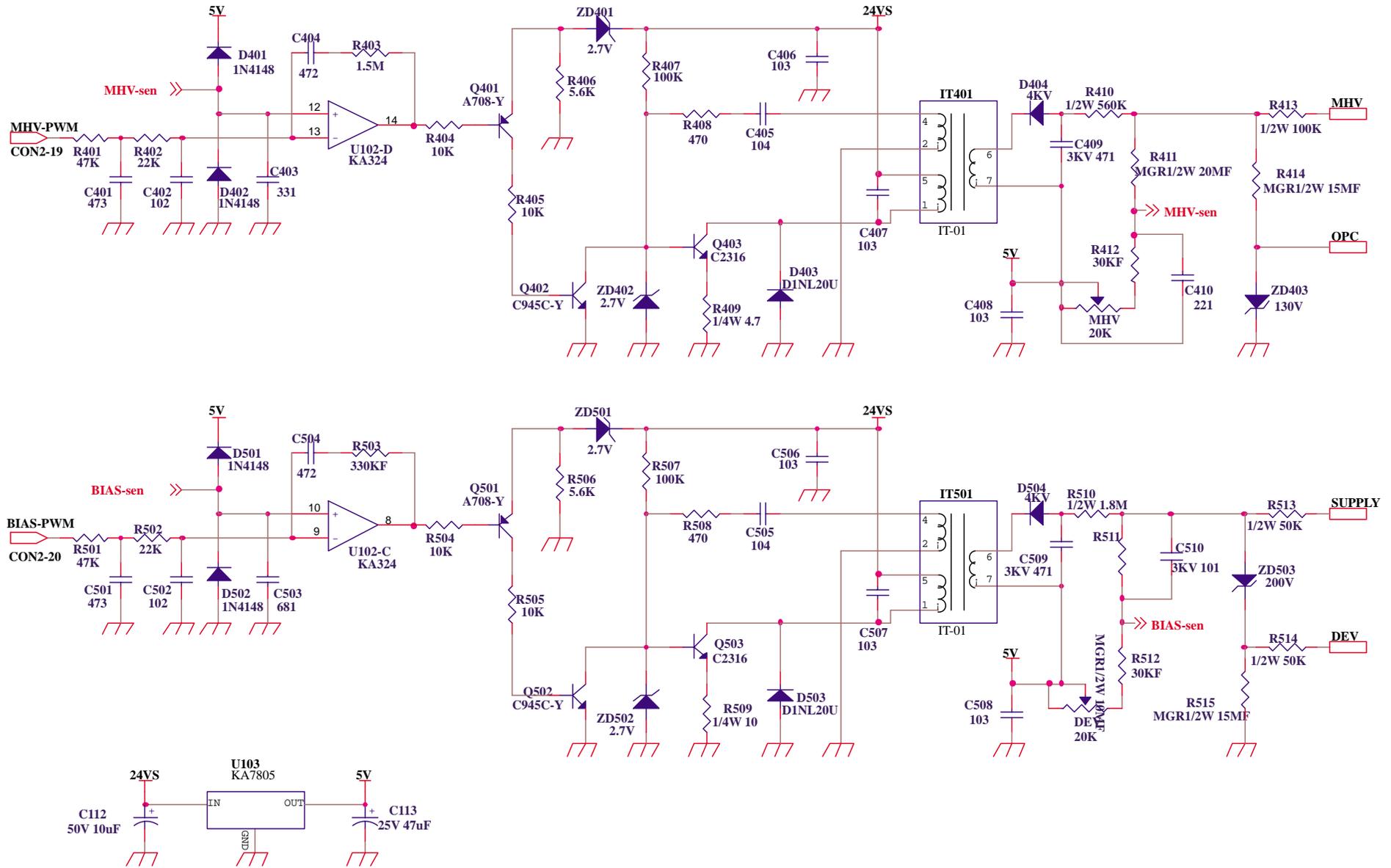


Engineer: JU KH	SAMSUNG		
Drawn By:	PRINTED BY		
R&D CHK:	HUMINGB163		
DOC CTRL CHK:	TITLE:	ML-1510/1710	1/2
MFG ENGR CHK:	CONTROLLER		
DATE: 2002.01.09	REV: 1.1	Drawing Number:	Page: 5

11.3 HVPS Circuit Diagram (1/2)



HVPS Circuit Diagram (2/2)



SMPS Circuit Diagram : 110V (2/2)

