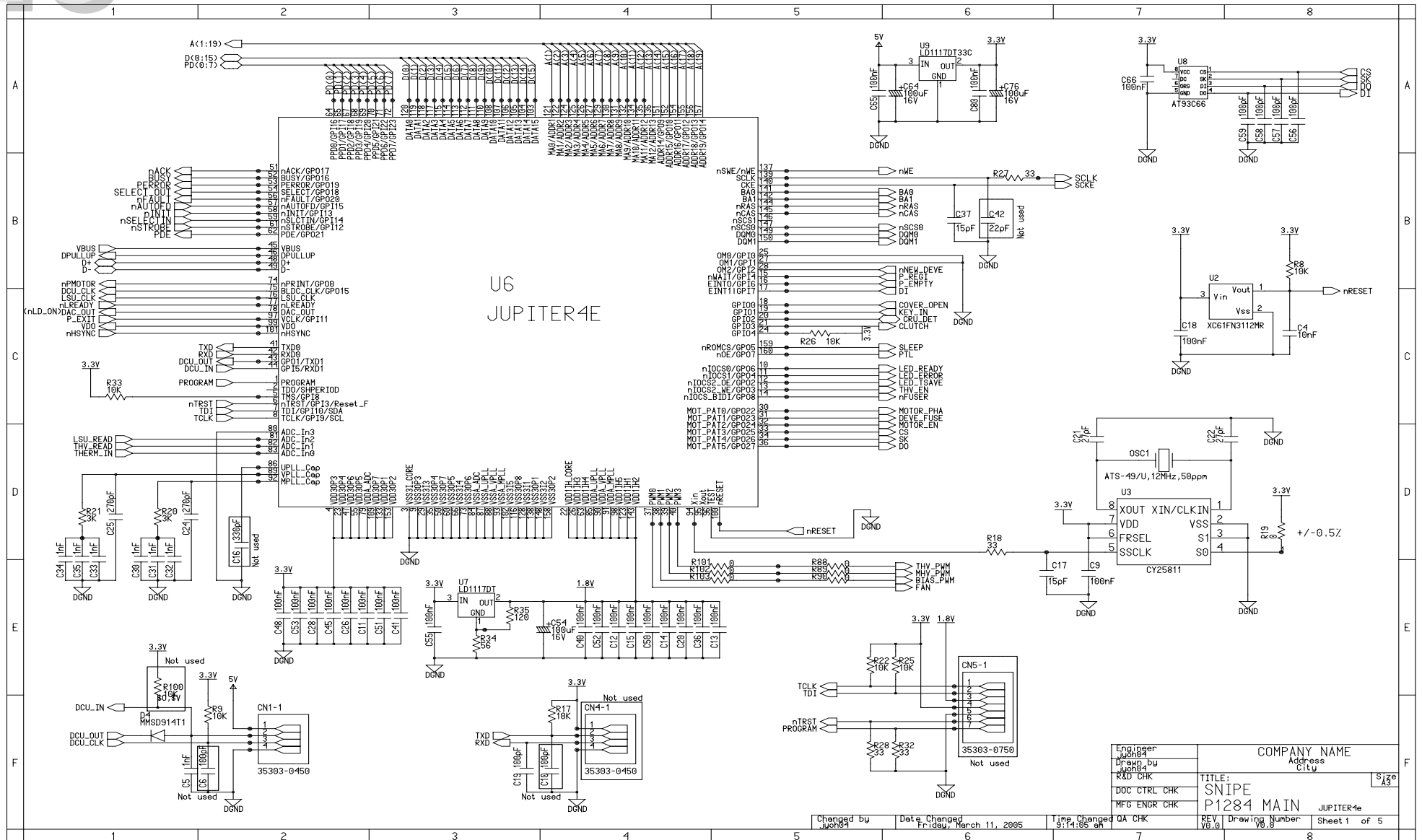


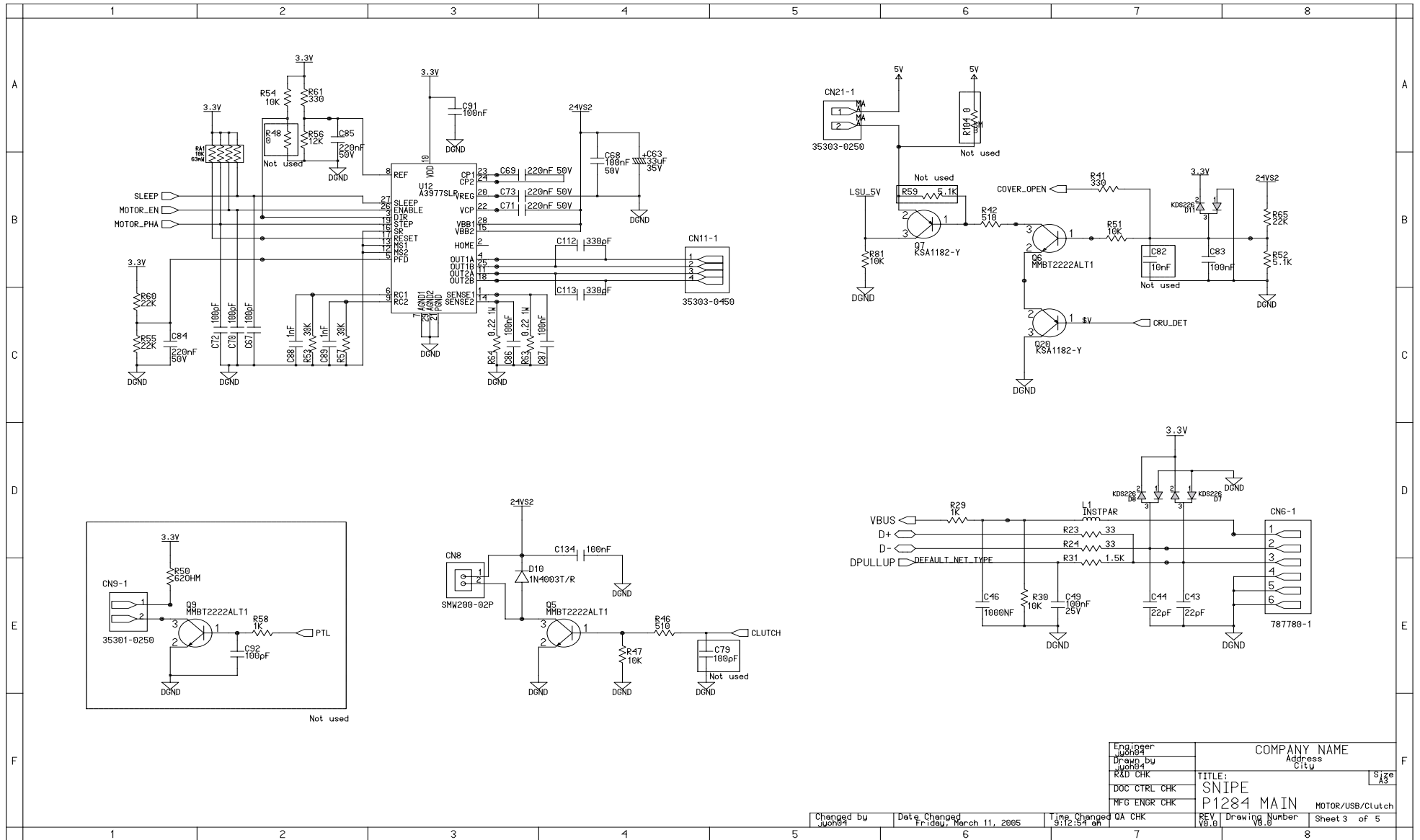
10. Schematic Diagrams

10.1 Main Board(1/5)

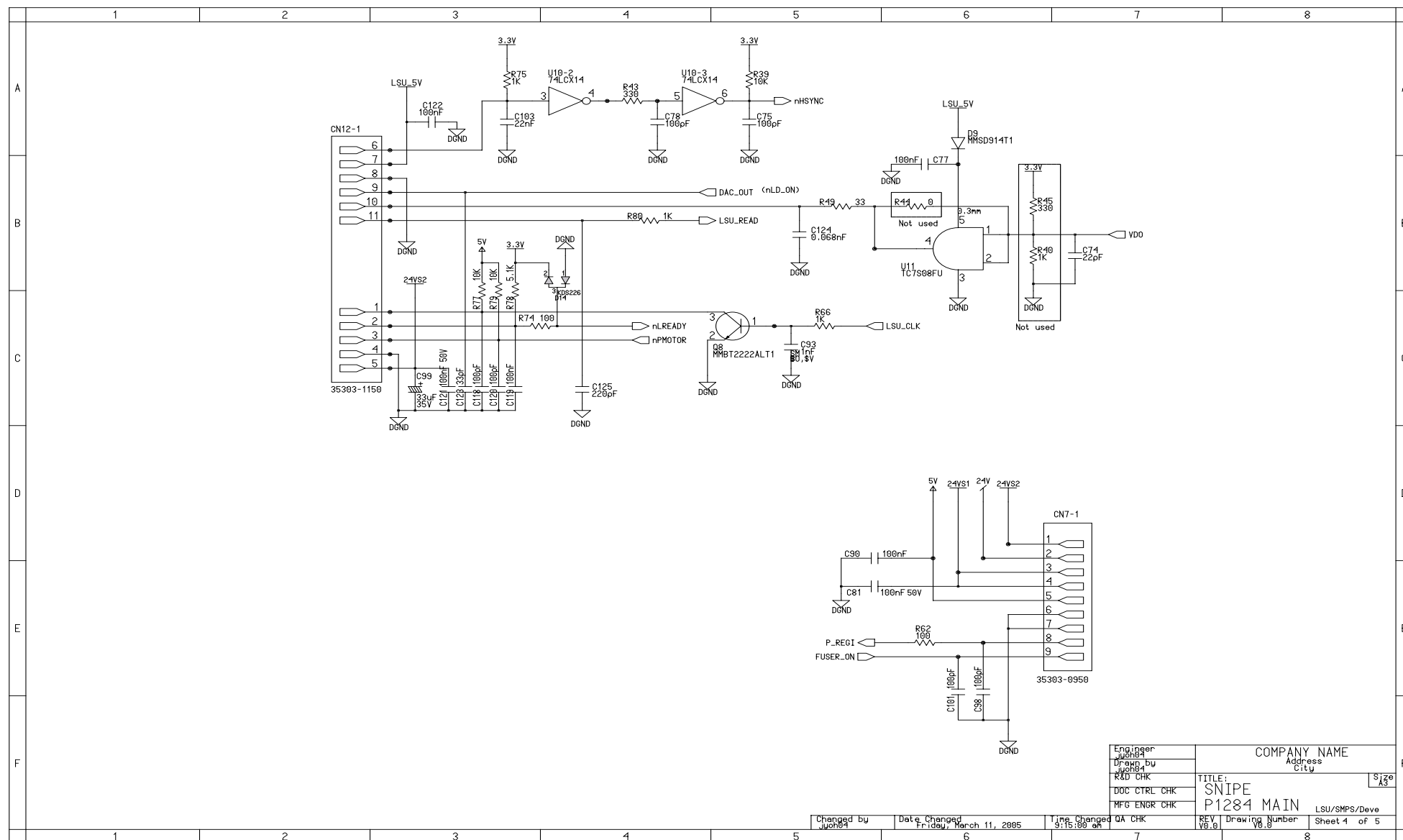




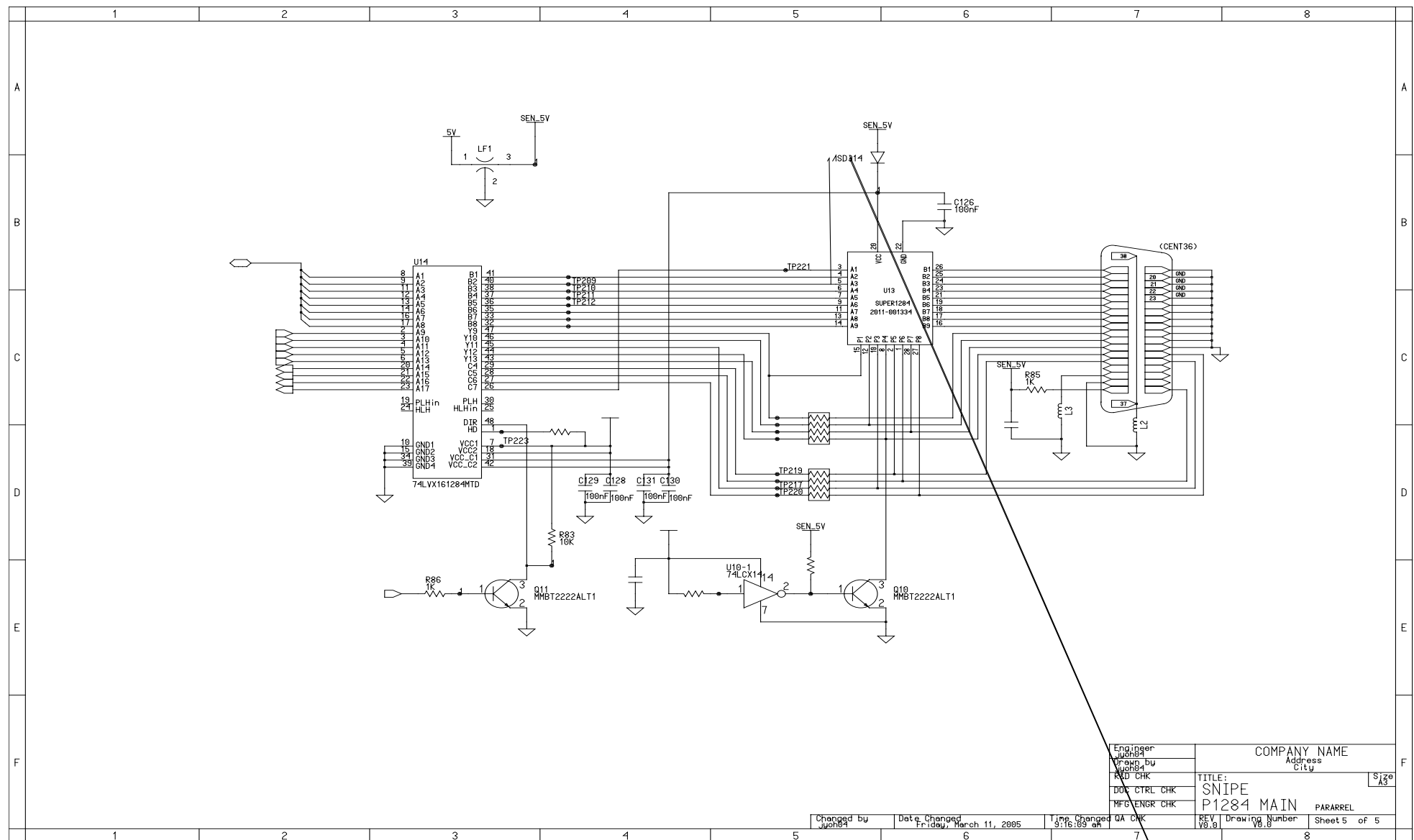
Main Board(3/5)



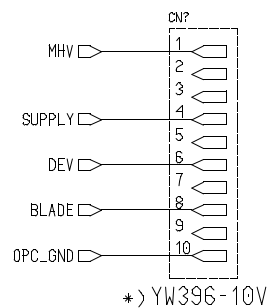
Main Board(4/5)



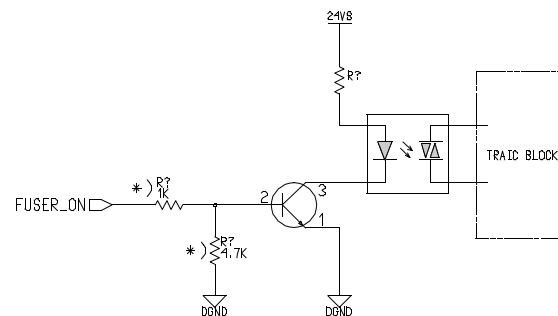
Main Board(5/5)



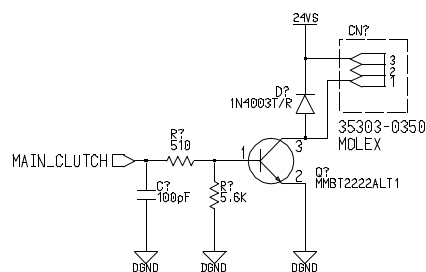
10-2 Connector Circuit Diagram



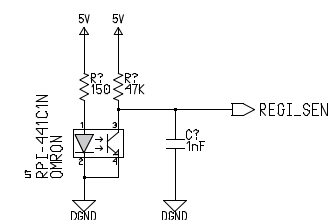
HVPS to DEVE



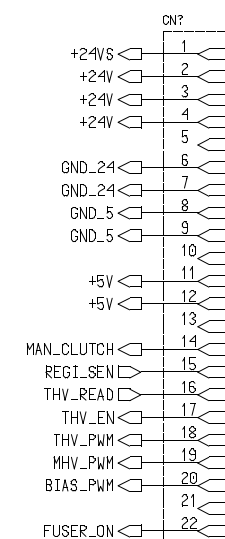
FUSER CONTROL



* MAIN CLUTCH

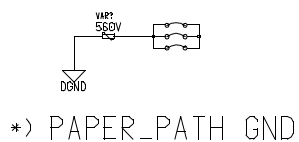


* REGI SENSOR

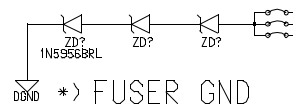


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* SMPS/HVPS TO MAIN



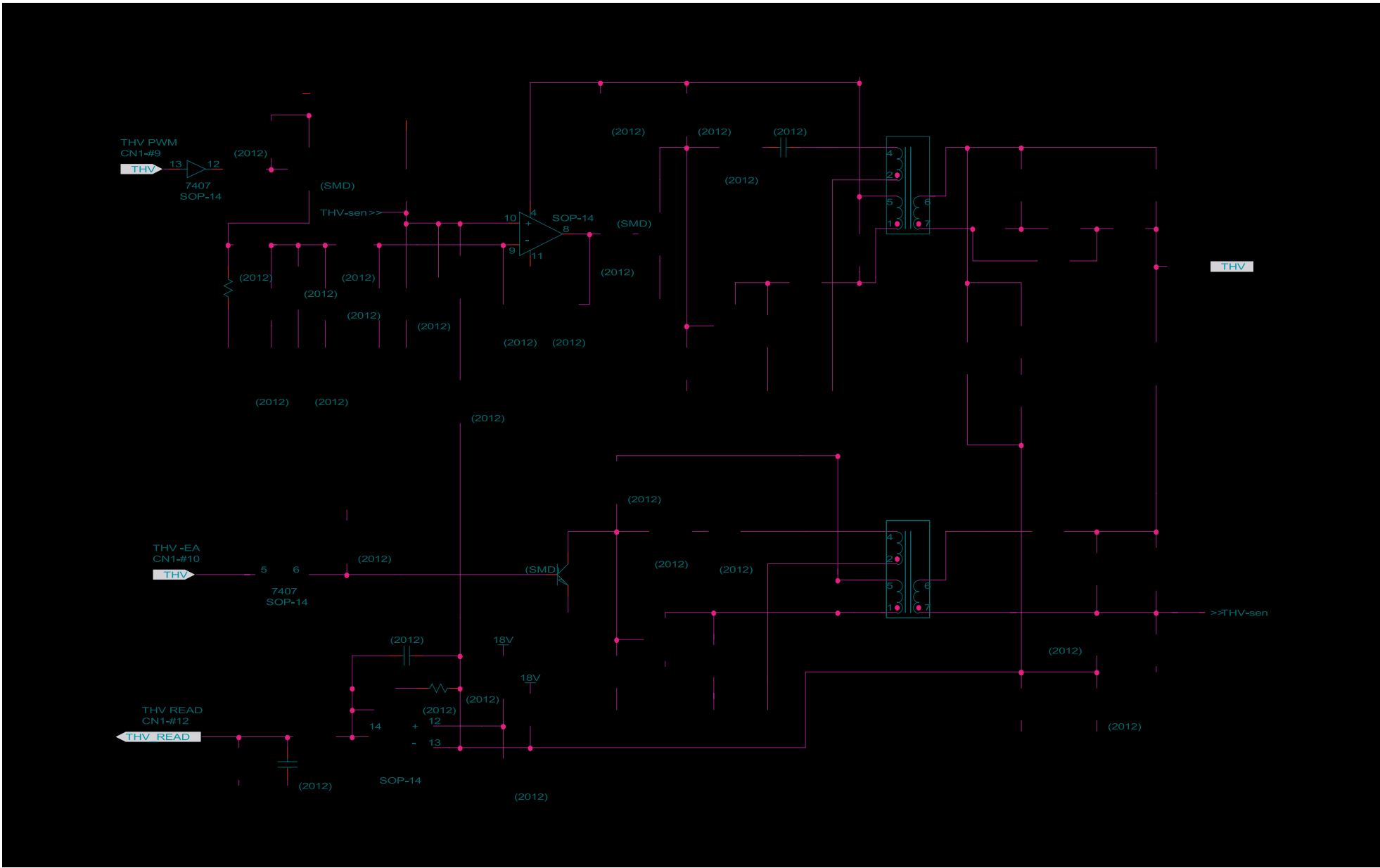
* PAPER_PATH GND



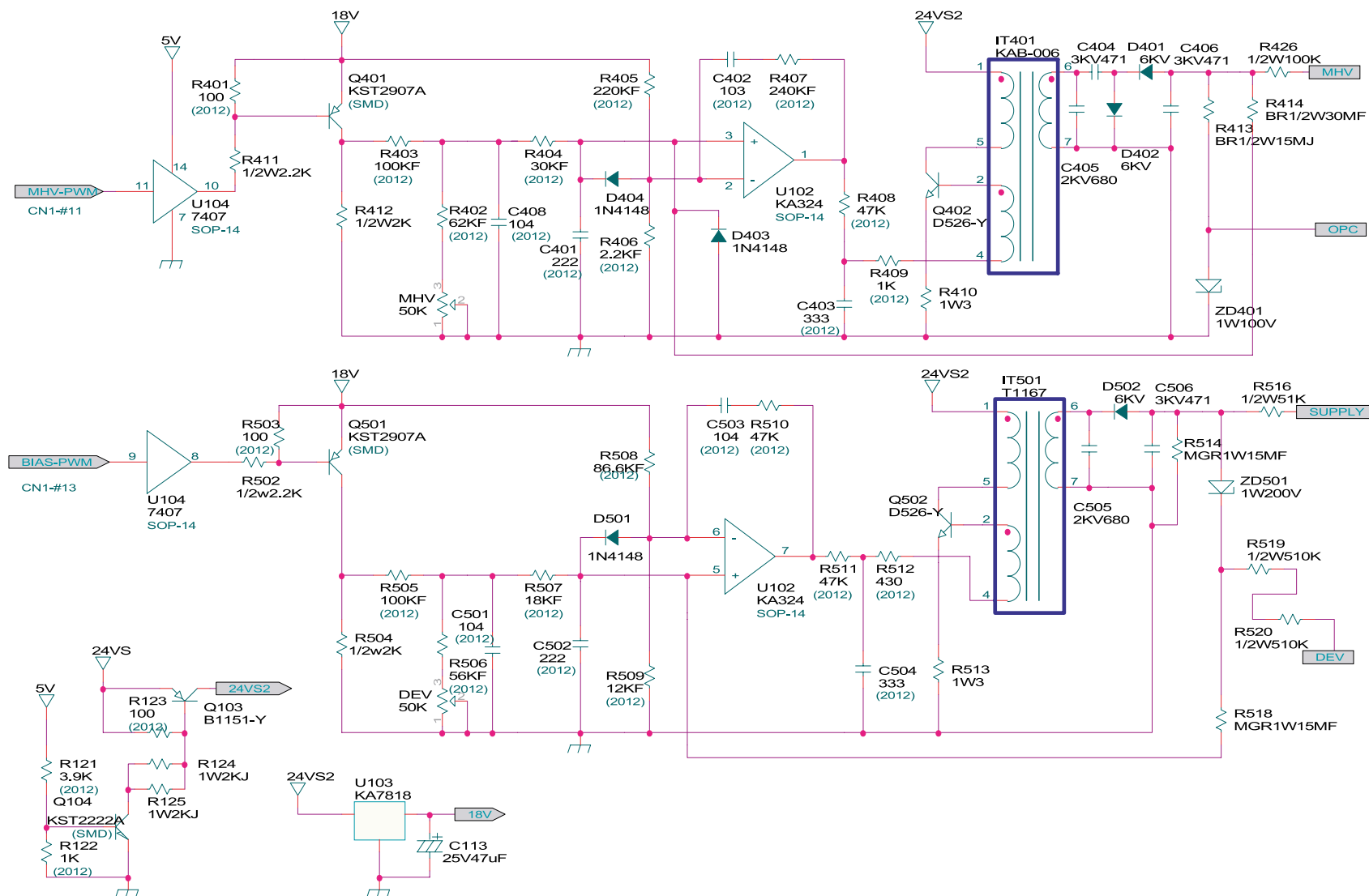
* FUSER GND



10-4 HVPS Circuit Diagram(1/3)



HVPS Circuit Diagram(2/3)



10-4 HVPS Circuit Diagram(3/3)

