

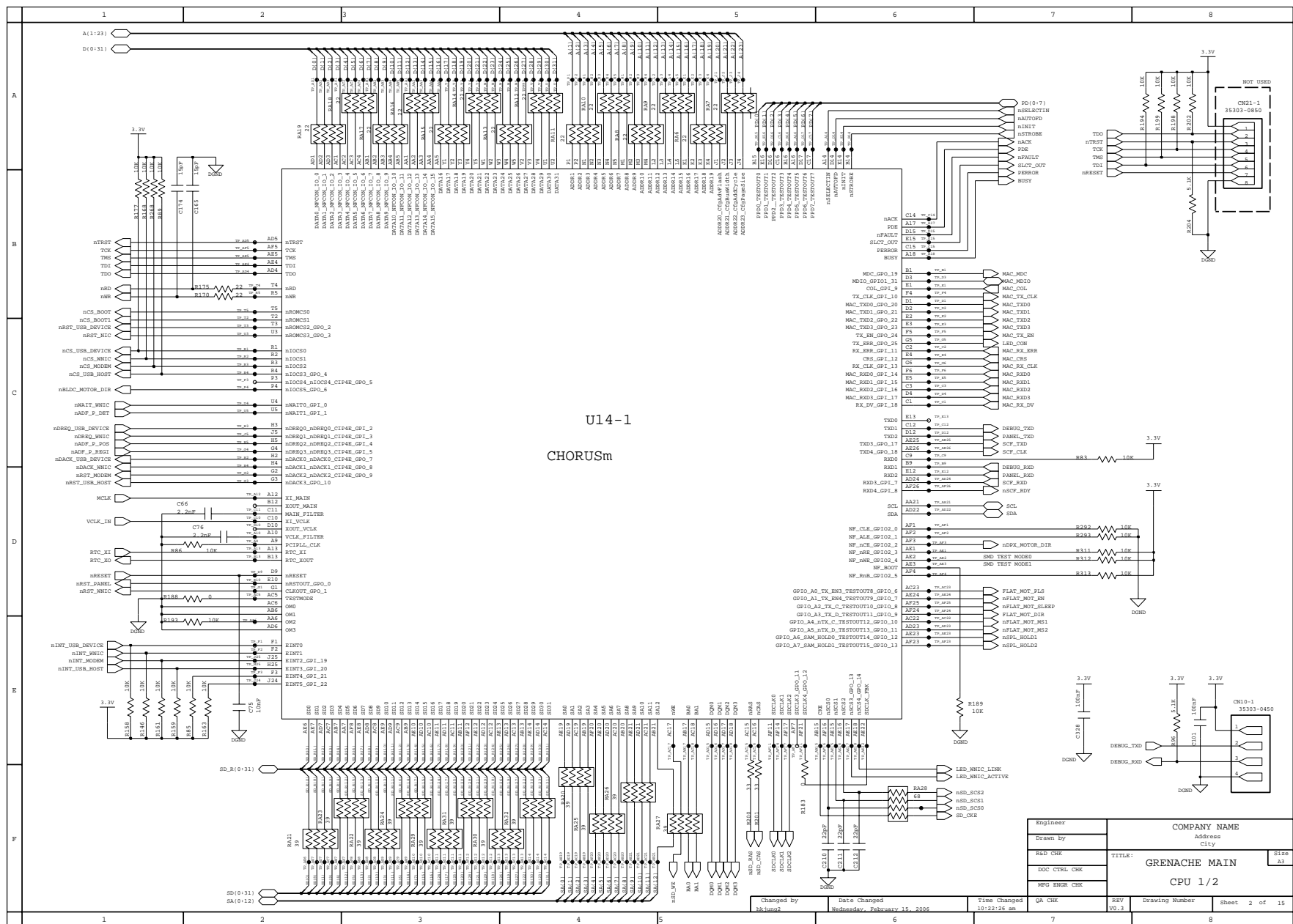
10. Schematic Diagrams

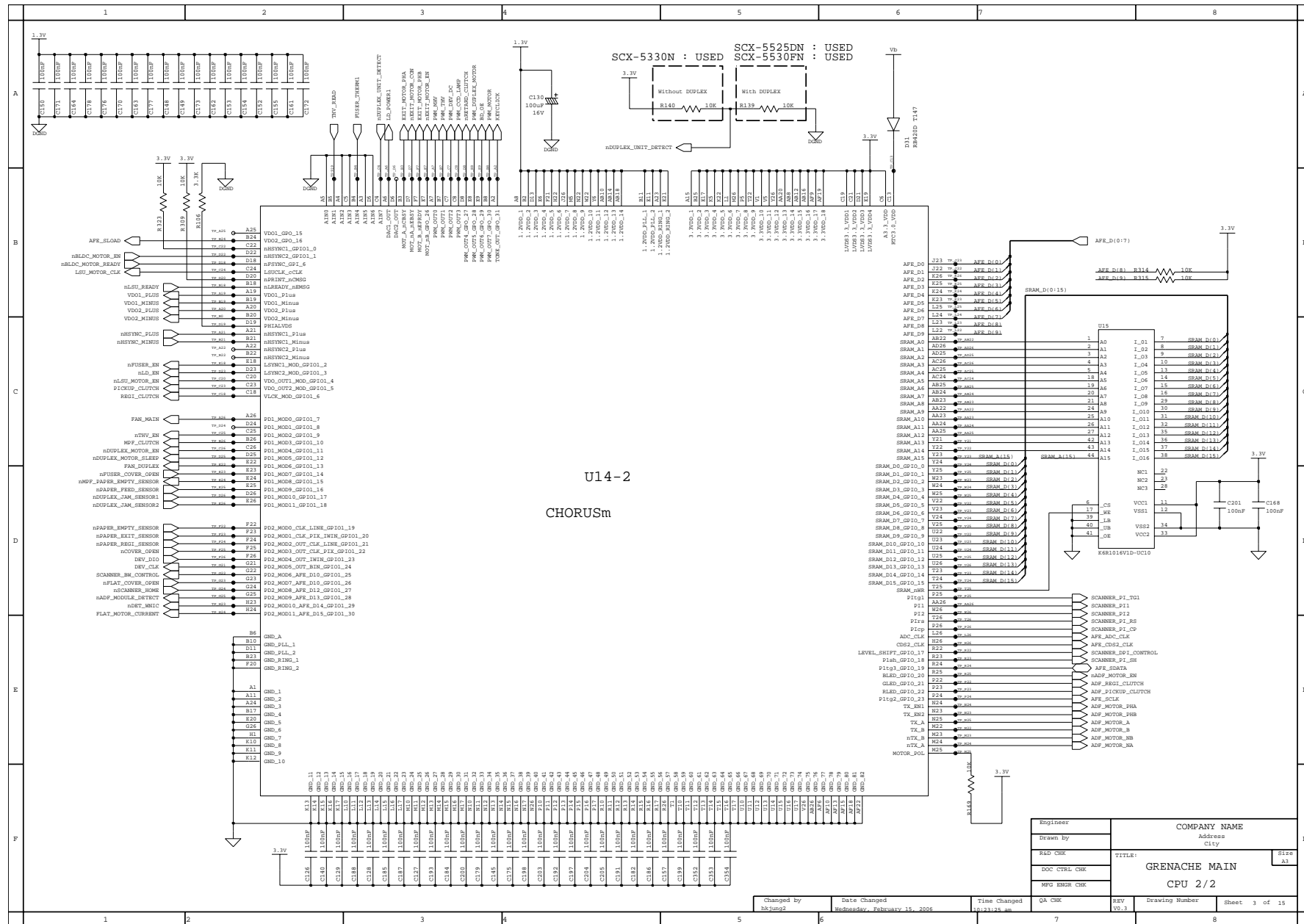
10.1 Schematic Main(1/15)

1	2	3	4	5	6	7	8
A							A
B							B
C							C
D							D
E							E
F							F

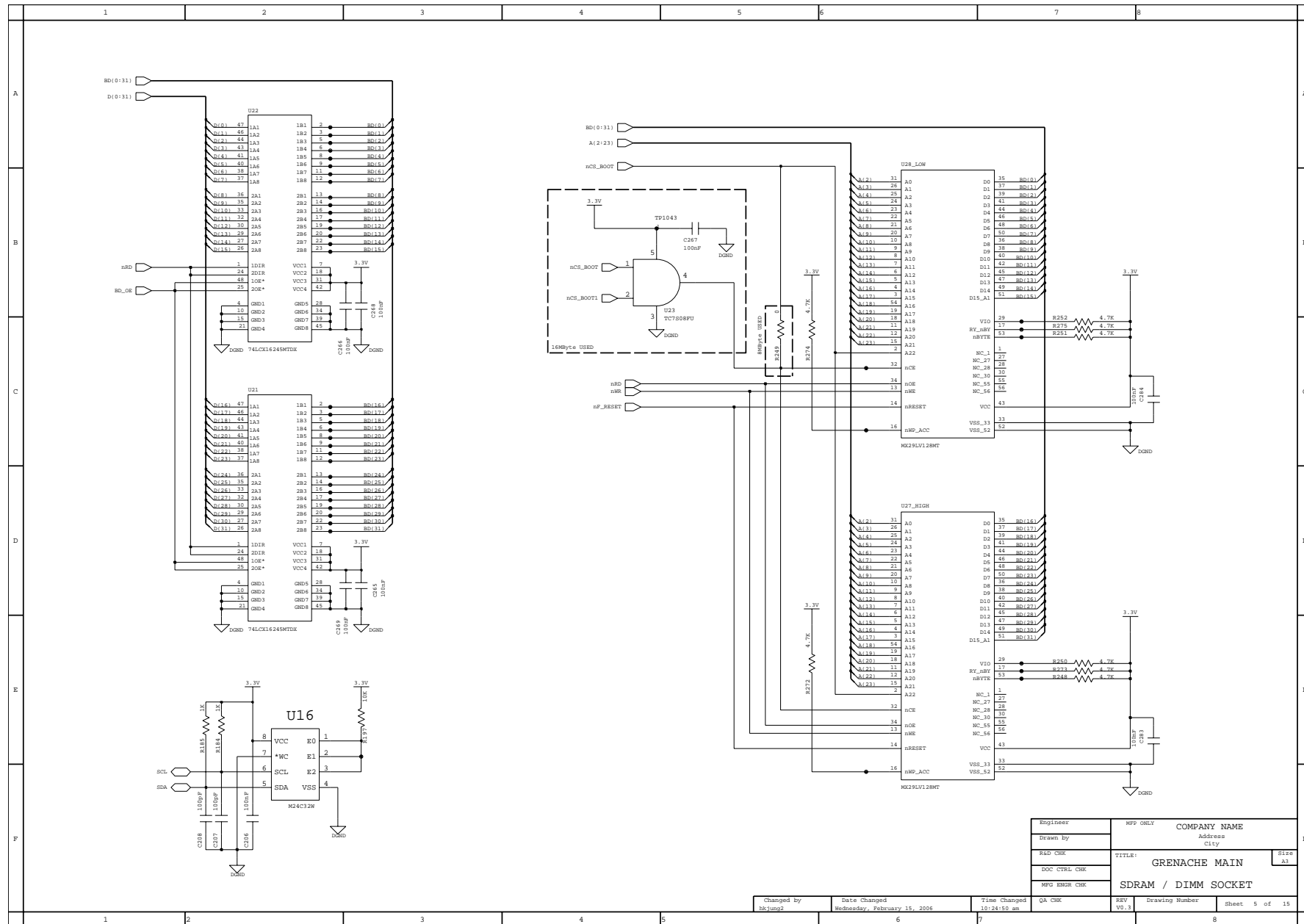
Page	Function
1	Index
2	CHORUSm(1) , JTAG , HYPER
3	CHORUSm(2) , 1.3V REGULATOR , SRAM
4	SDRAM , DIMM SOCKET
5	NOR FLASH MEMORY , DELAY BUFFER , SYSTEM EEPROM
6	EMBEDDED NIC , WLAN SOCKET
7	USB HOST CONTROLLER
8	USB 2.0 DEVICE CONTROLLER
9	IEEE1284 INTERFACE
10	CLK , RESET , BATTERY CHARGER
11	CCD INTERFACE , CCD LAMP PWM CONTROLLER
12	LSU INTERFACE , MODEM INTERFACE , COVER OPEN SENSOR
13	ENGINE INTERFACE , FUSER CONTROLLER , BLDC MOTER INTERFACE
14	EXIT MOTOR , DUPLEX MOTOR , FLATEN MOTOR , DUPLEX INTERFACE
15	ENGINE INTERFACE , SCF INTERFACE

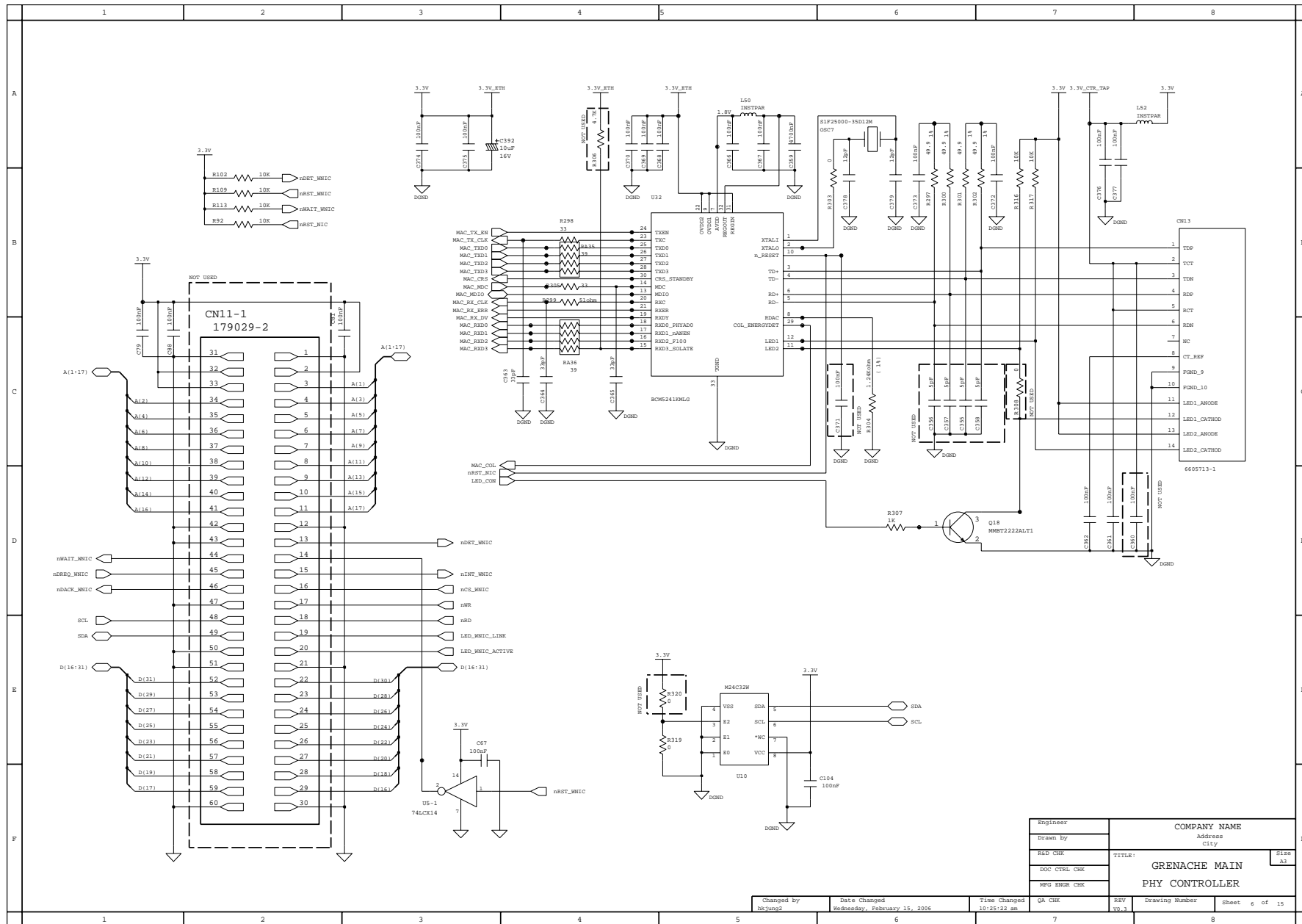
Engineer	COMPANY NAME	
Drawn by	Address	
Rev. CHK	City	
DOC. CTRL. CHK	TITLE:	Sheet
INFO. ENG. CHK	GRENACHE MAIN	11
	INDEX	
Changed by	Date Changed	Time Changed
bkjung2	Thursday, December 15, 2005	9:05:26 am
QA CHK	REV	Drawing Number
	00.1	Sheet 1 of 15

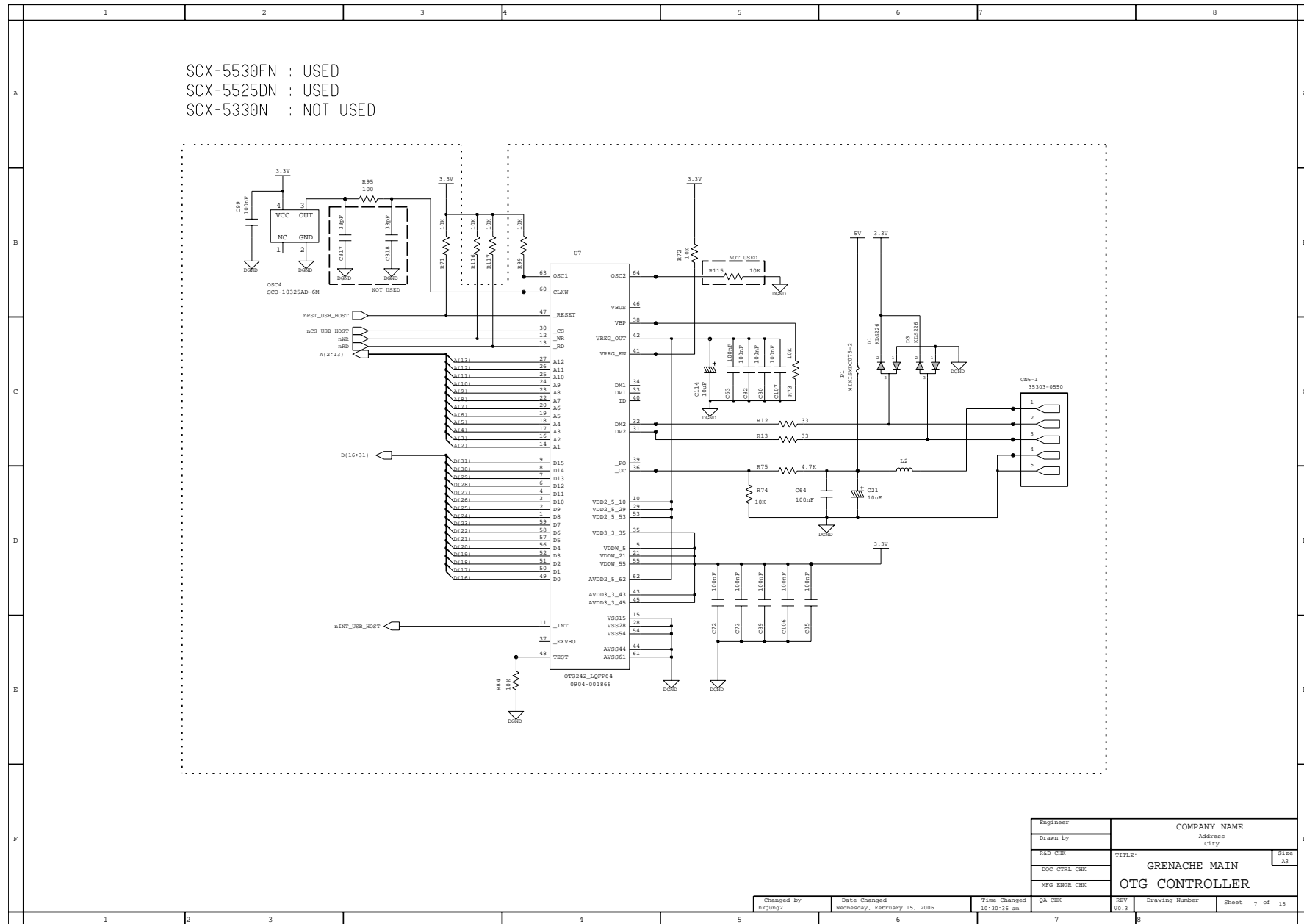


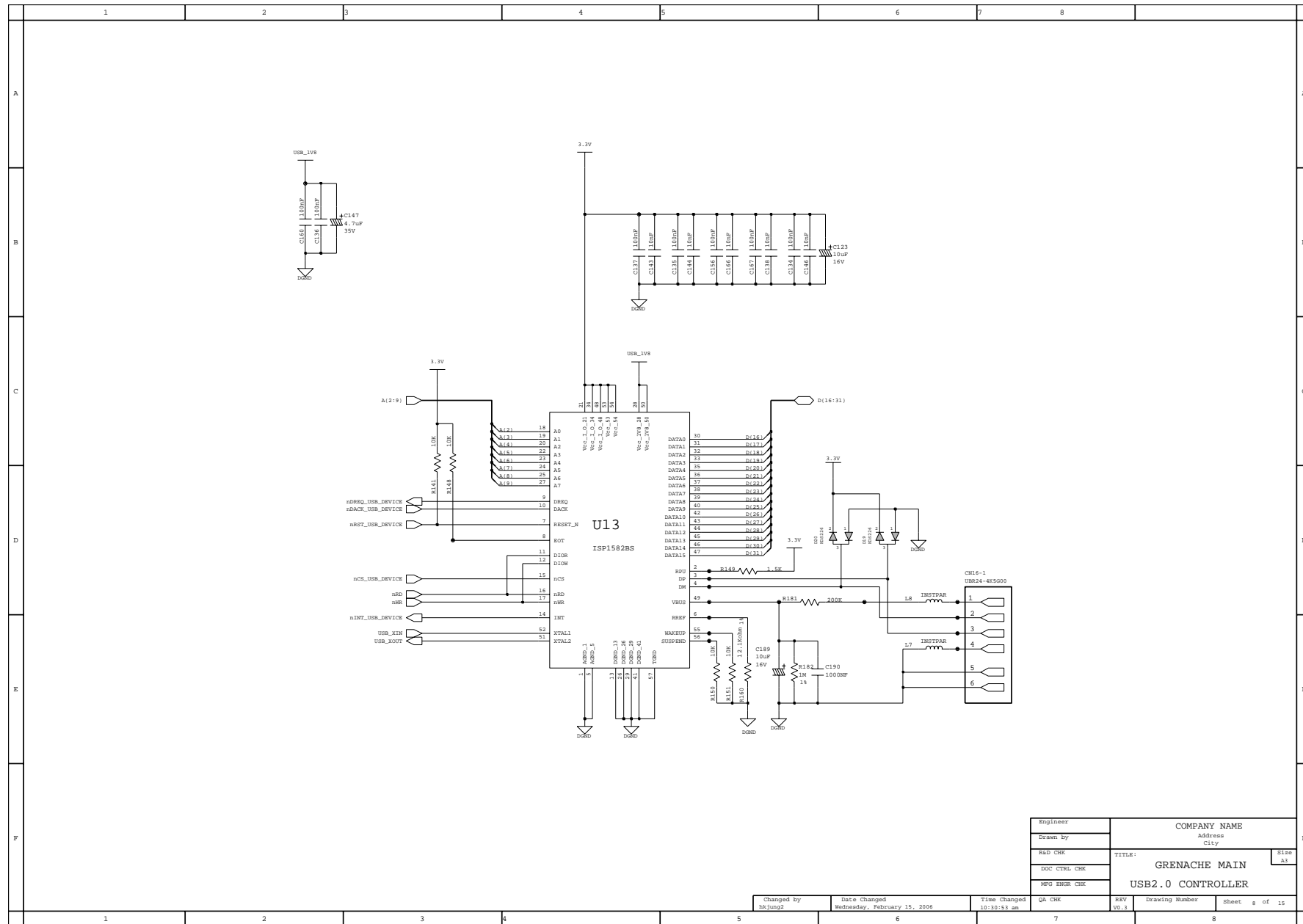




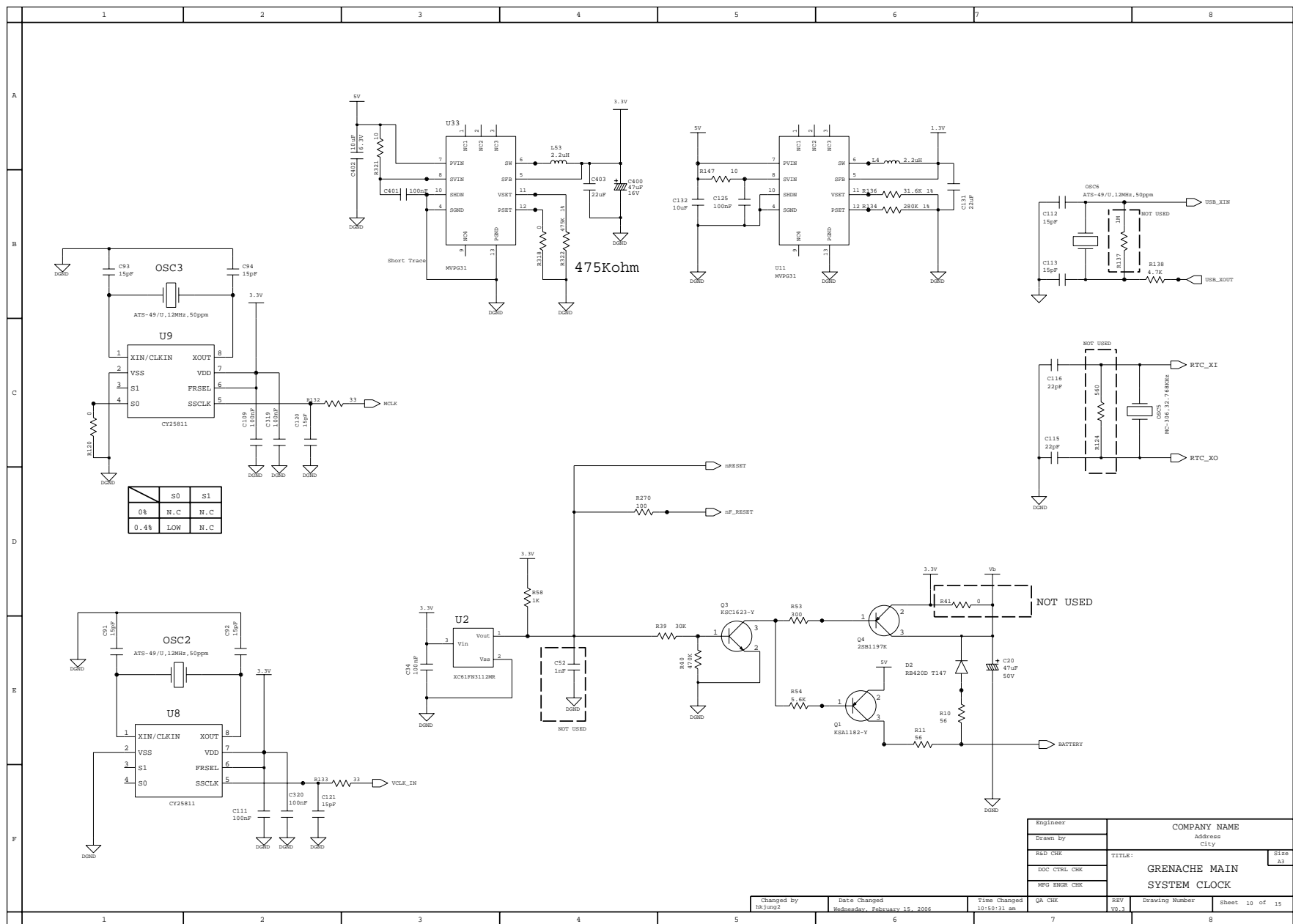




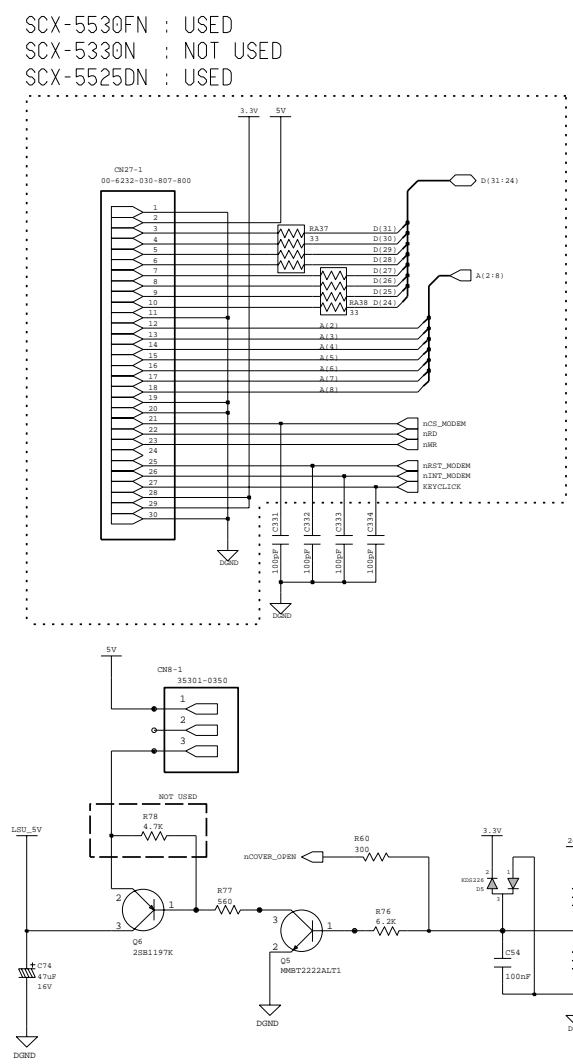




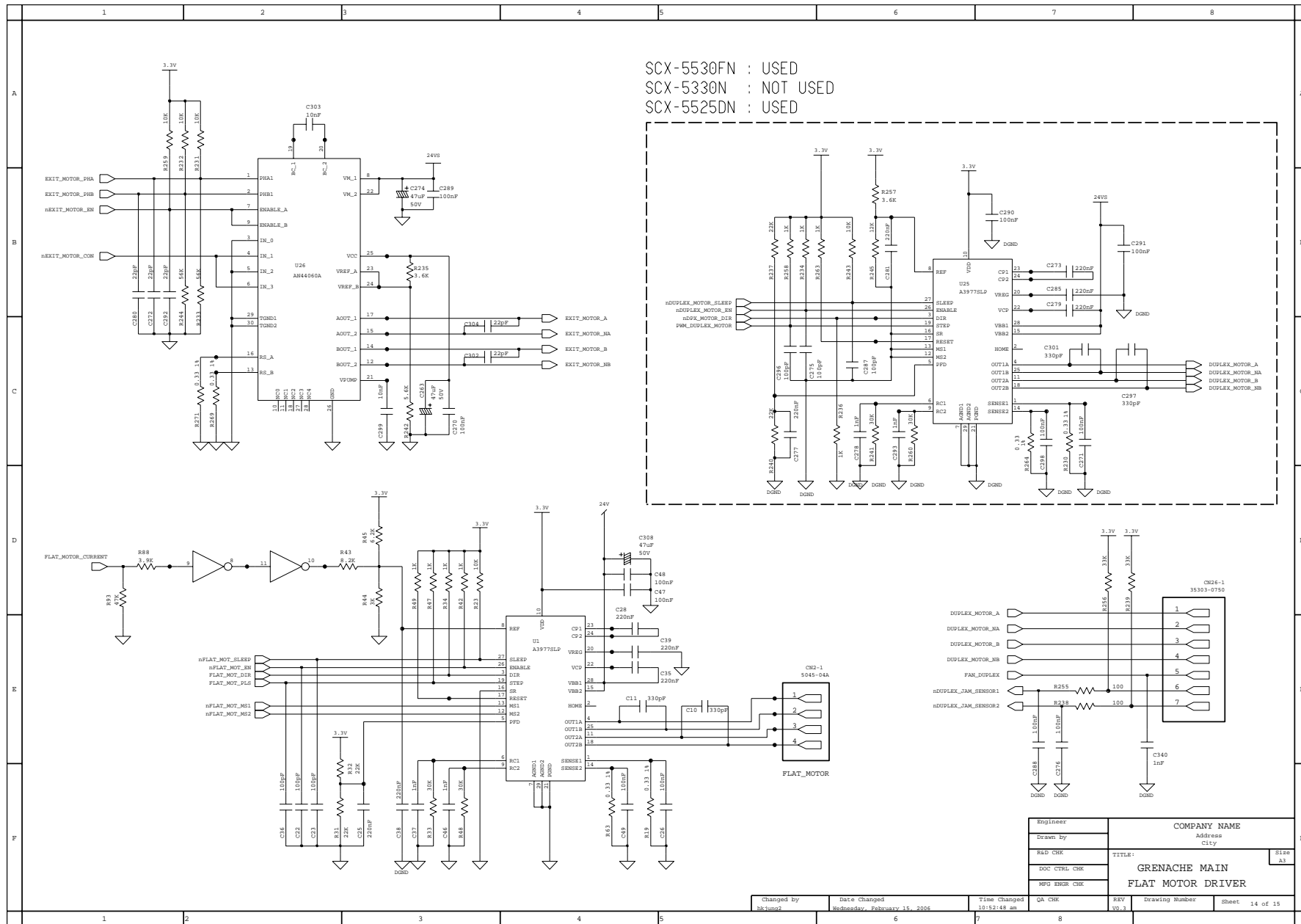






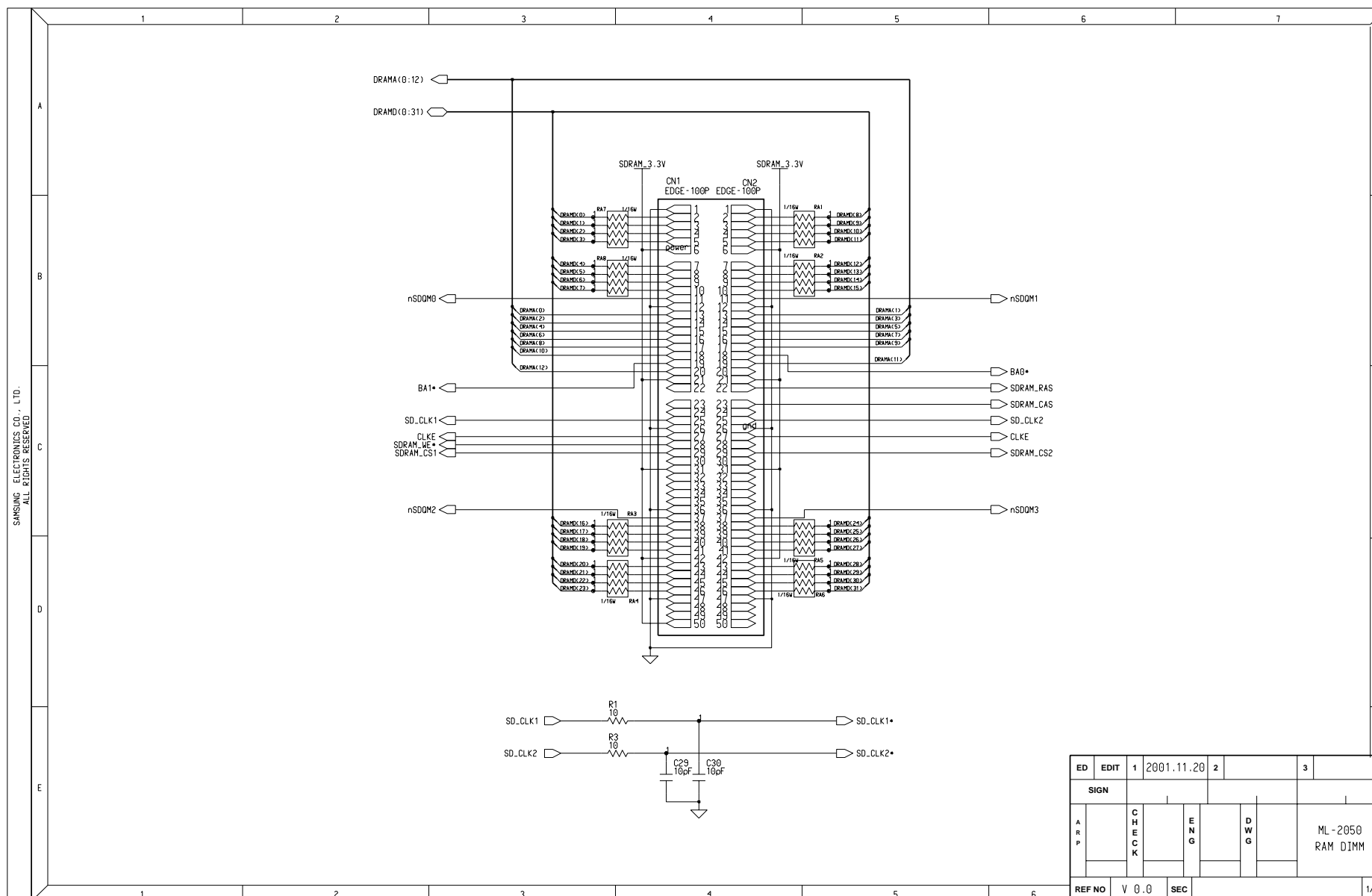


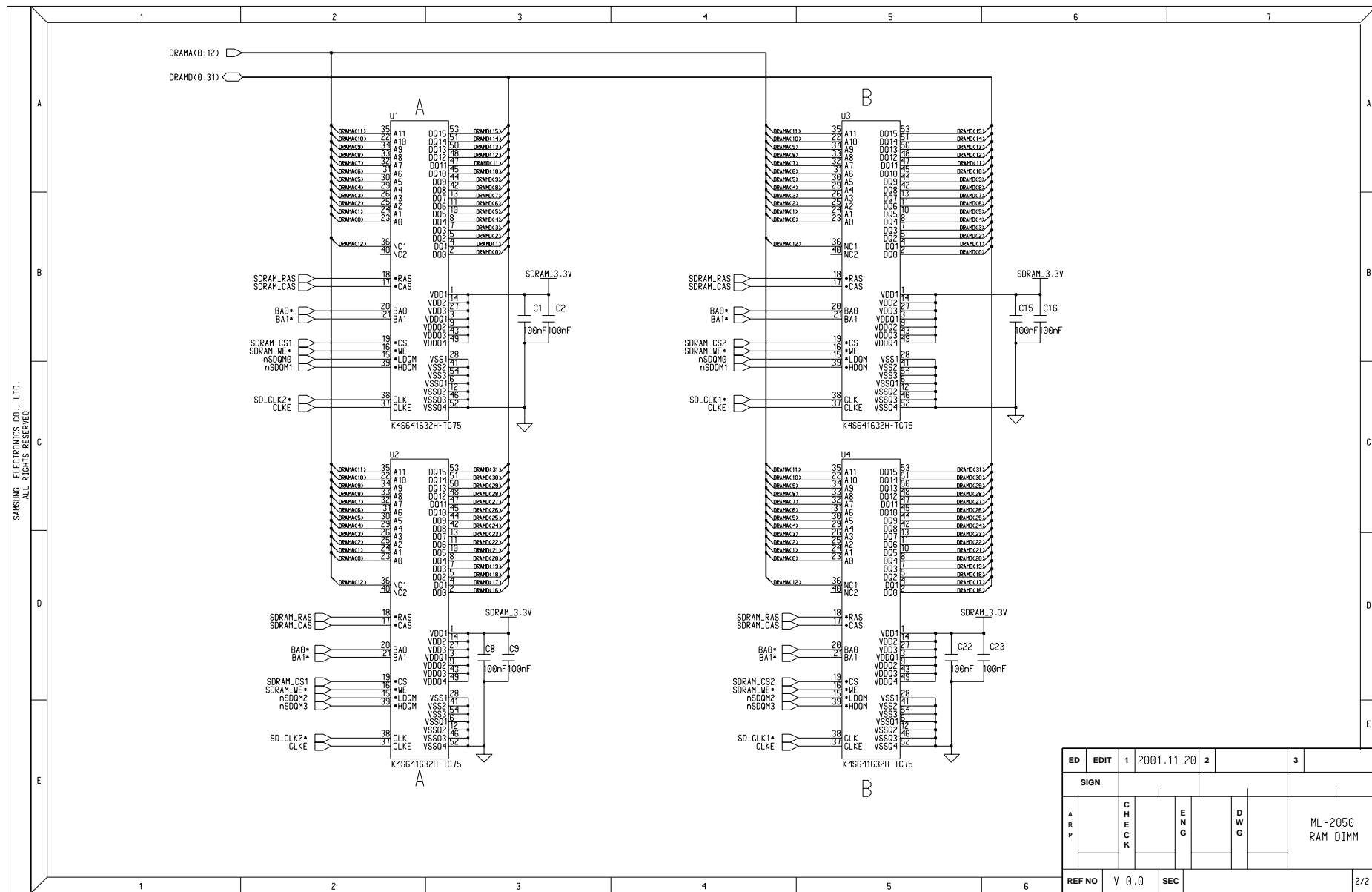




Schematic Diagram

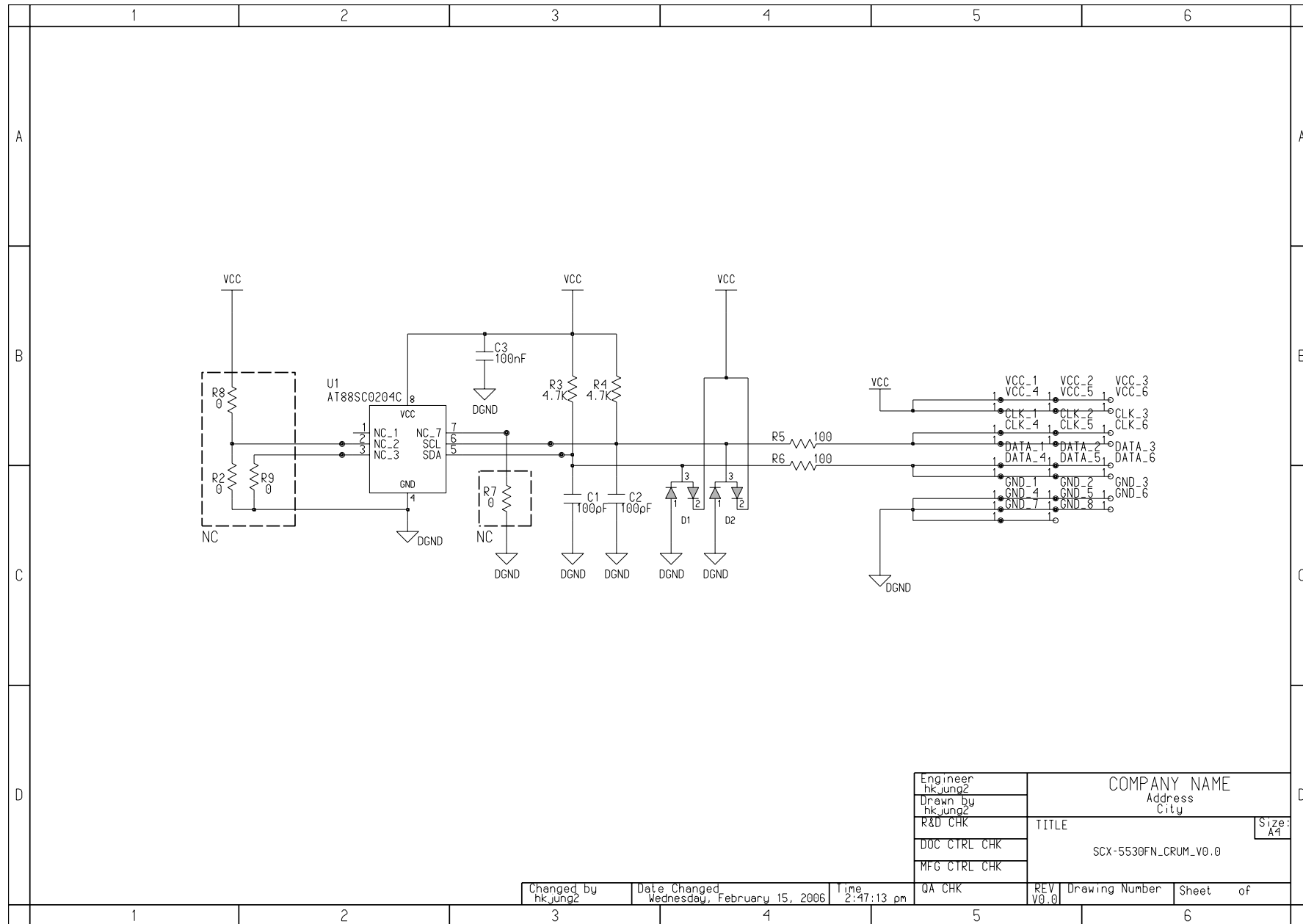
Schematic Diagram





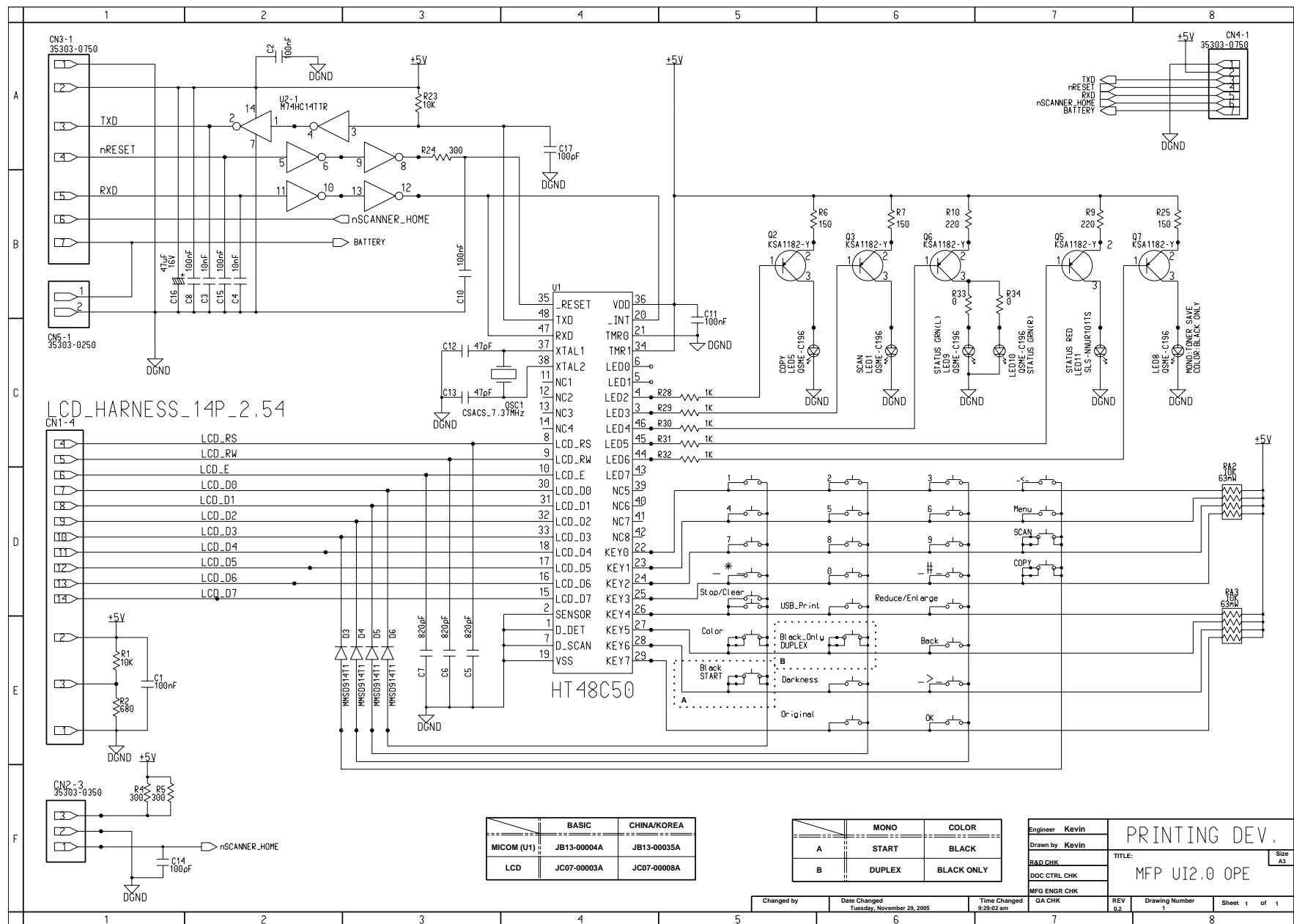
10.3 CRUM board

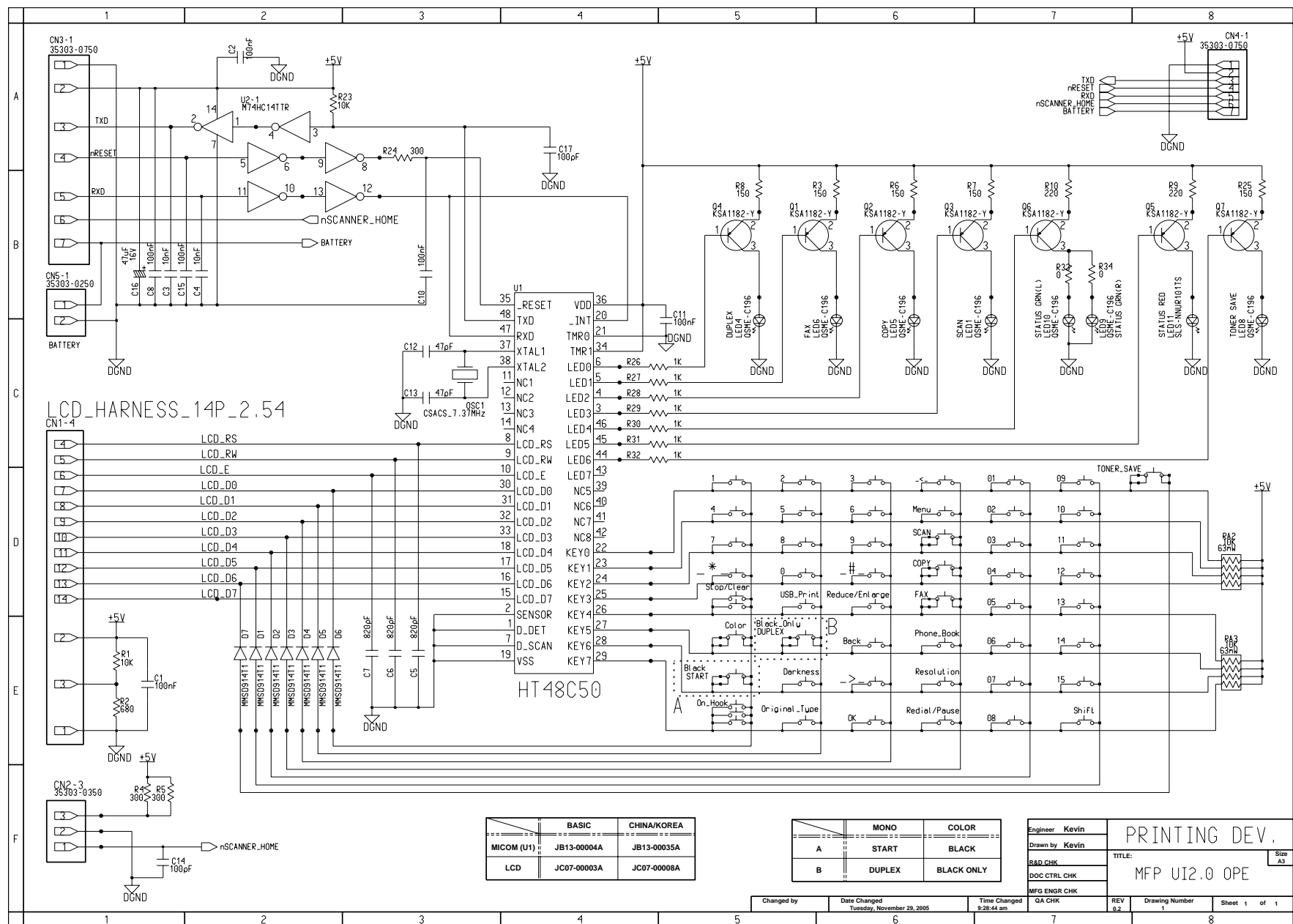
Schematic Diagram

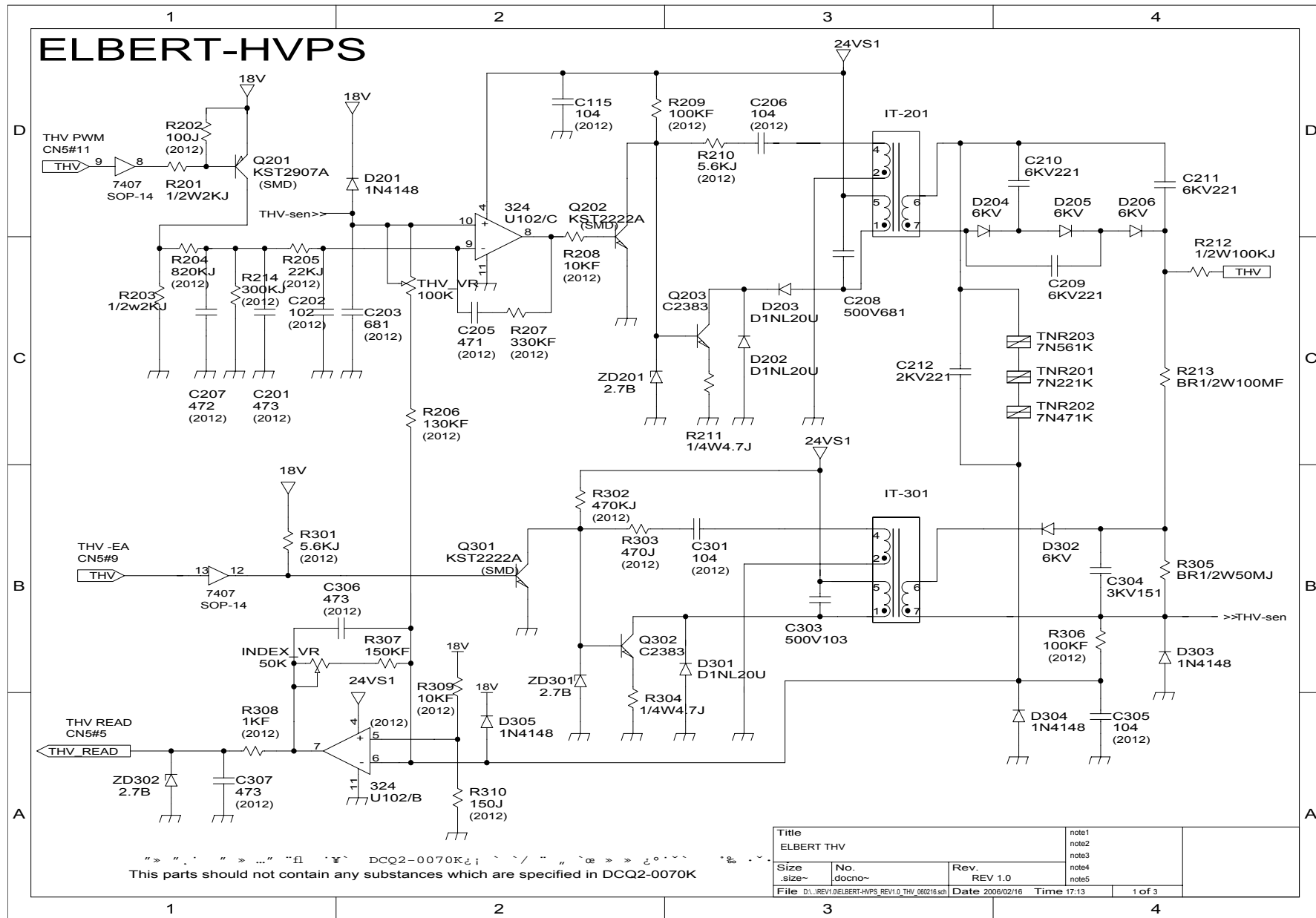


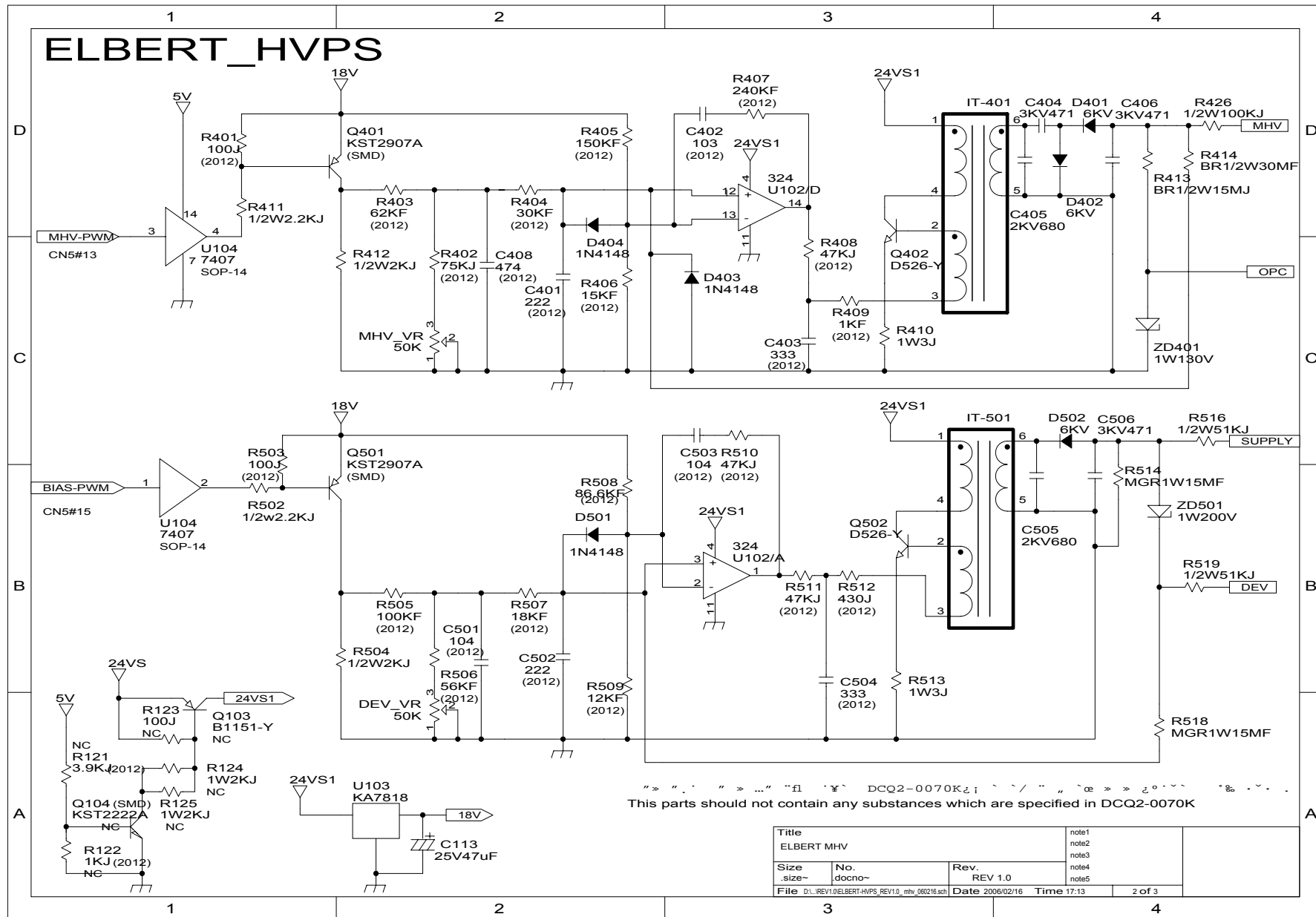
10.4 OPE_3in1

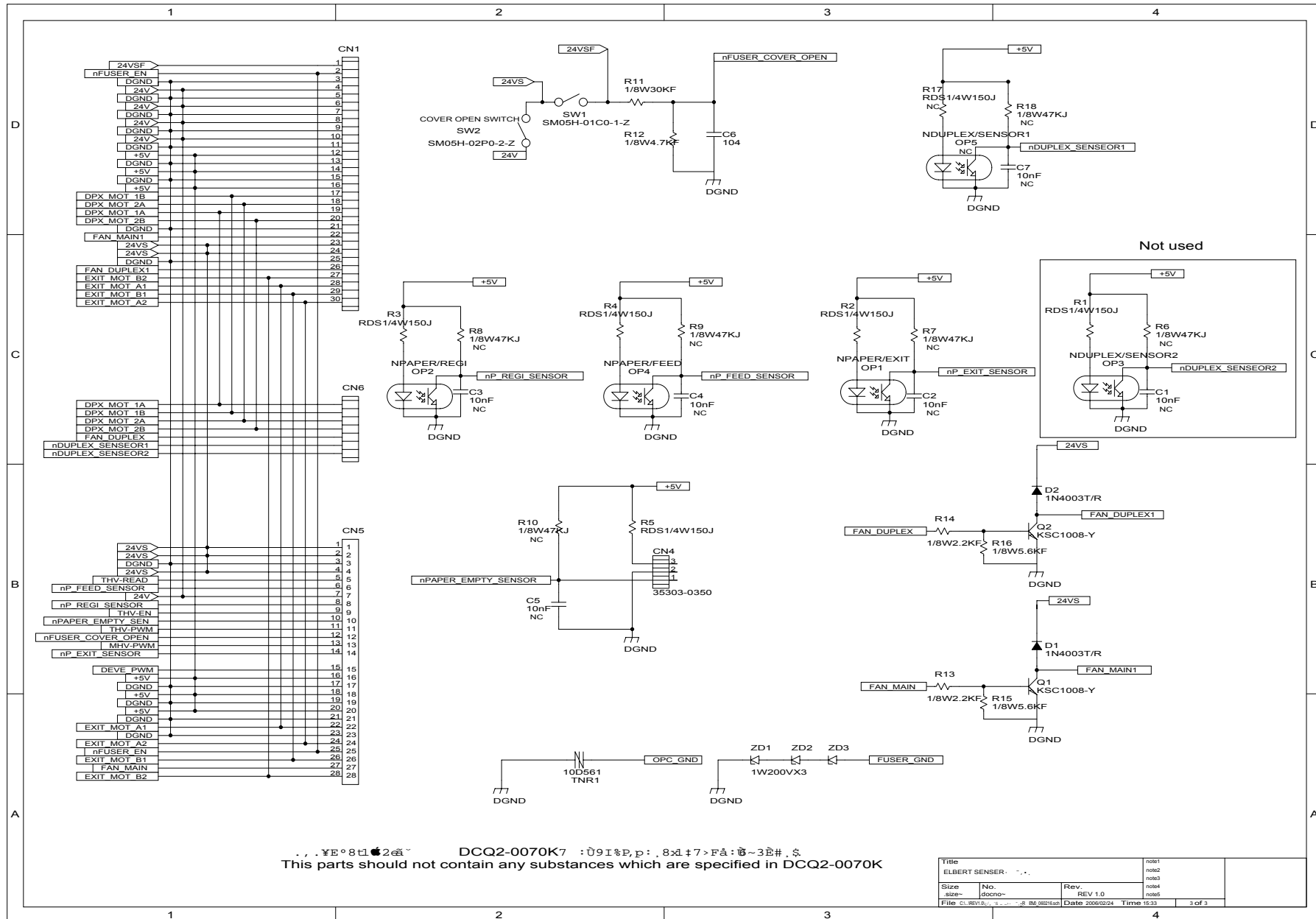
Schematic Diagram

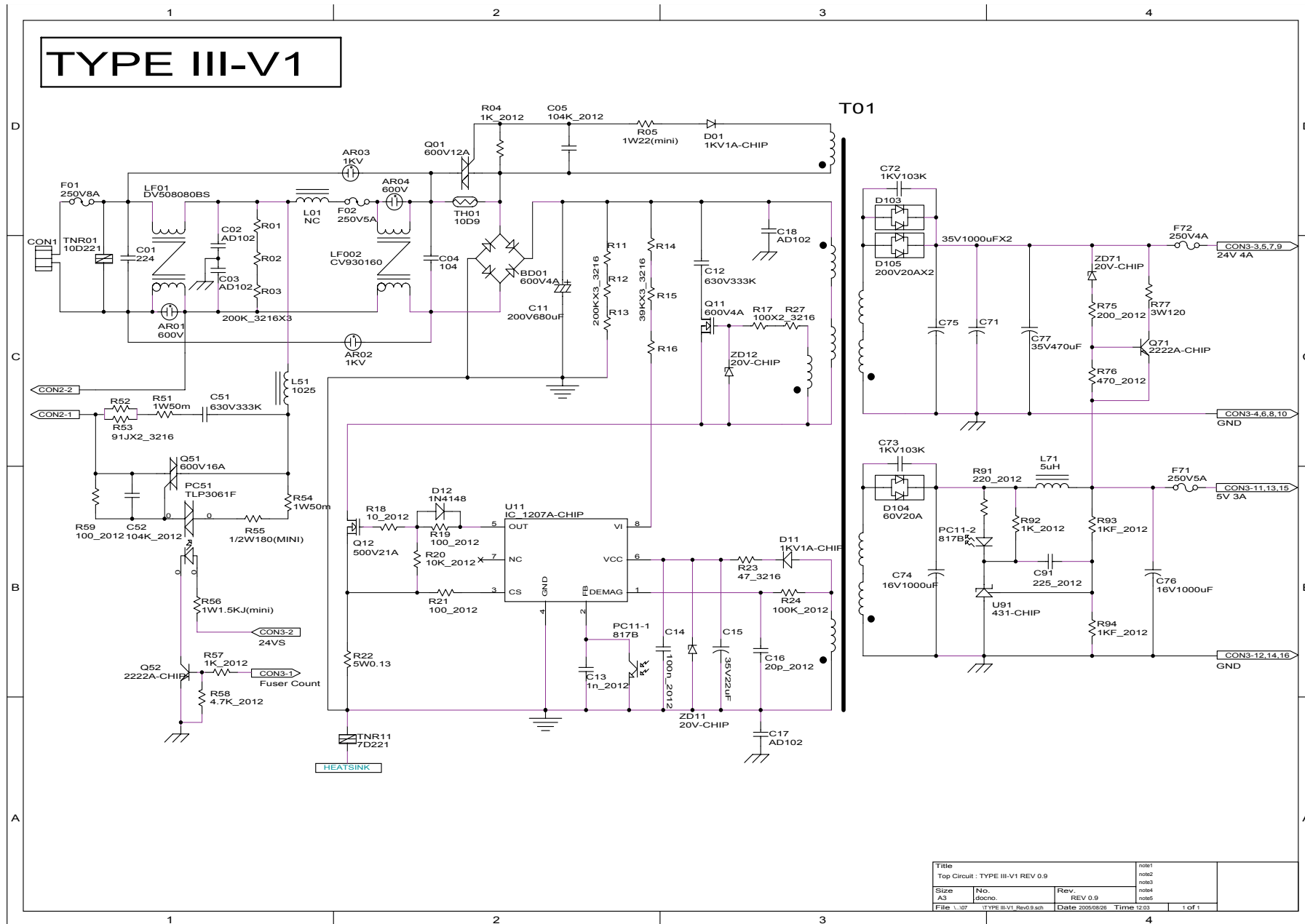




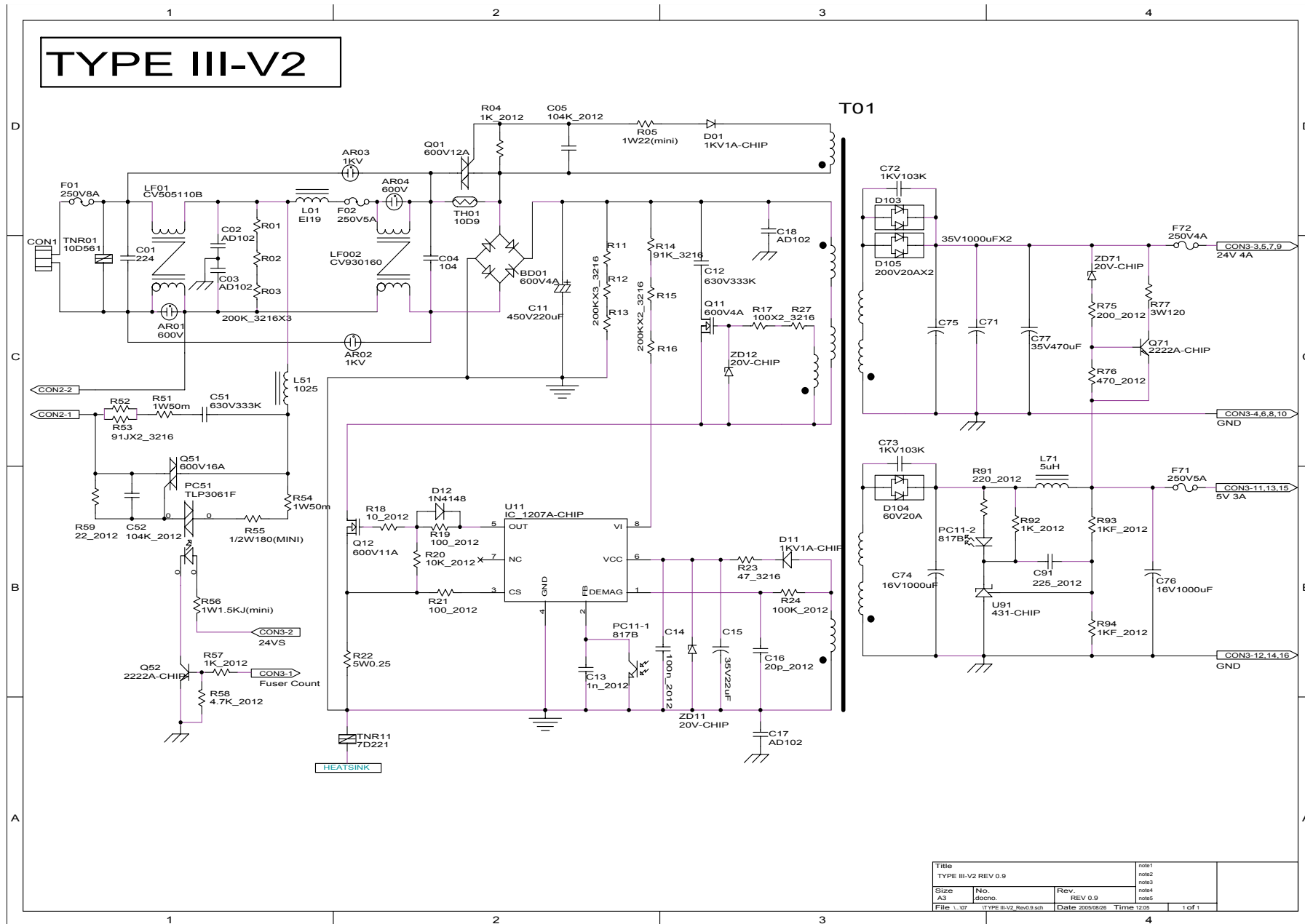








Title		Top Circuit : TYPE III-V1 REV 0.9		note1
Size	No.	Rev.	REV 0.9	note2
A3	05000	Date	20050826	note3
File	1_07	Time	12:03	note4
1 of 1				



Schematic Diagram

10-26 *Service Manual*

Samsung Electronics

