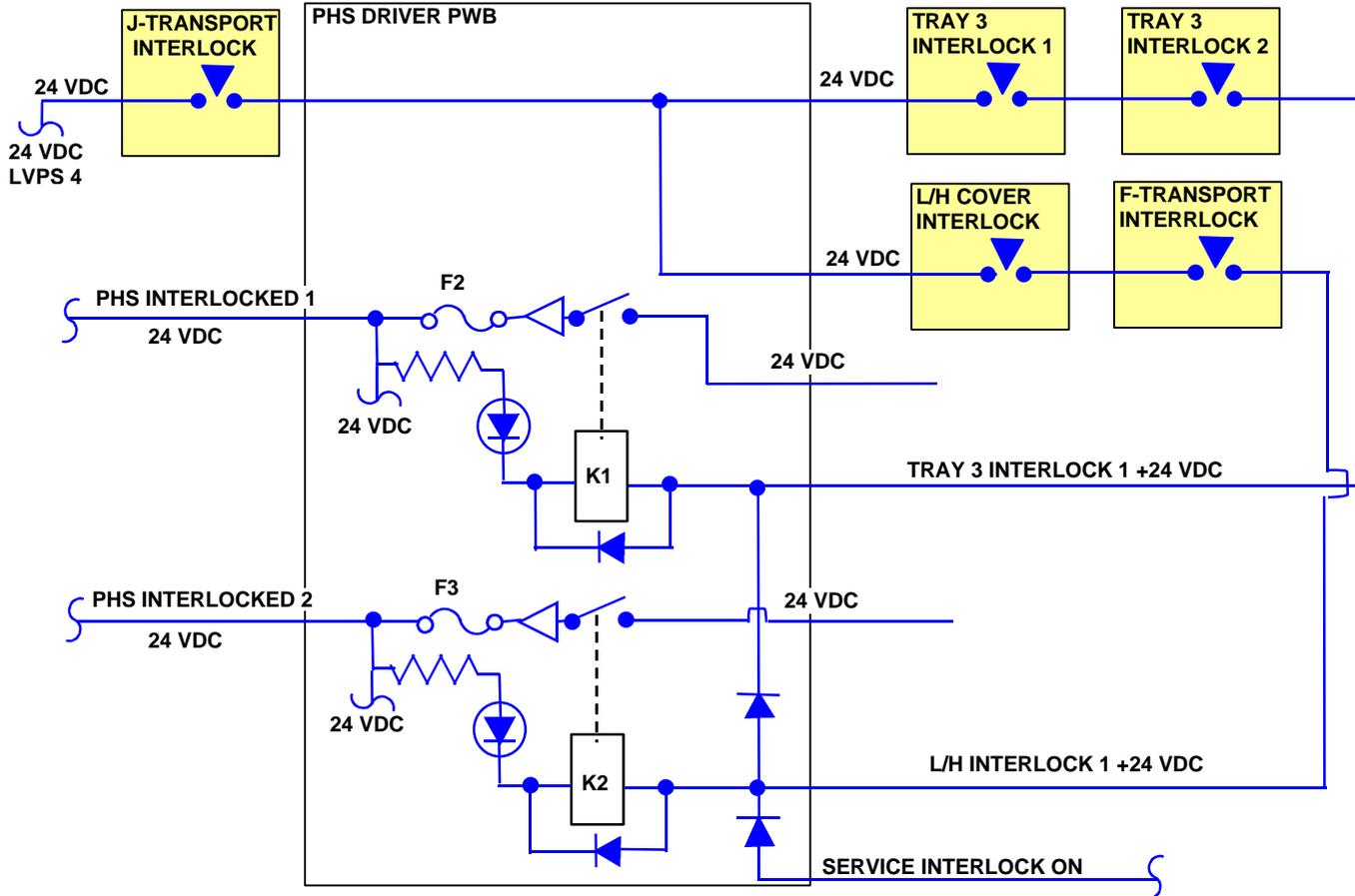


Interlock Switching - Electrical Module

Paper Handling Module Interlocks (24 VDC)

The Paper Handling Interlocks disable 24 VDC and 5 VDC when the PH Module Interlocks are open. The loss of 24 VDC enables the PHS Interlocked 1 & 2 signal. The loss of 5 VDC disables the PHS Driver PWB, IOT PWB and Distribution 1/2 PWB. (See Paper Handling Module 5 VDC Interlocks diagram on next page.)



Interlock Switching - Electrical Module

Interlock Switching - Paper Handling Interlocks (5 VDC)

